

CPU-Z

CPU

Caches

Mainboard

Memory

SPD

Graphics

About

L1 D-Cache

Size

32 KBytes

x 2

Descriptor

8-way set associative, 64-byte line size

L1 I-Cache

Size

32 KBytes

x 2

Descriptor

8-way set associative, 64-byte line size

L2 Cache

Size

6144 KBytes

Descriptor

24-way set associative, 64-byte line size

L3 Cache

Size

Descriptor

Size

Descriptor

CPU-Z

Version 1.59

Validate

OK