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2024-01-02 17:05:52 - Attempting to retrieve the root directory of the data partition.
2024-01-02 17:05:52 - Found 5 handles that supported SimpleFileSystem
2024-01-02 17:05:52 - Checking handle 0 (D96C4518)
2024-01-02 17:05:52 - Handle is own partition
2024-01-02 17:05:52 - Checking handle 1 (D96C3998)
2024-01-02 17:05:52 - HD node found: MBR type(2), Signature type(2), Part #(2), Start LBA(524288),
Num Sectors(524288)
2024-01-02 17:05:52 - Self node: MBR type(2), Signature type(2), Part #(1), Start LBA(2048), Num
Sectors(522240)
2024-01-02 17:05:52 - Checking handle 2 (D96C2698)
2024-01-02 17:05:52 - Checking handle 3 (D96C1798)
2024-01-02 17:05:52 - Checking handle 4 (D9522518)
2024-01-02 17:05:52 - Found 10 handles that supported Block I/O protocol
2024-01-02 17:05:52 - HD node found: MBR type(2), Signature type(2), Signature(50415353-CE04-4F8F-
AC32-6F3693F43704), Part #(1), Start LBA(2048), Num Sectors(522240)
2024-01-02 17:05:52 - HD node found: MBR type(2), Signature type(2), Signature(50415353-6343-46C0-
B68C-69EE6326F32B), Part #(2), Start LBA(524288), Num Sectors(524288)
2024-01-02 17:05:52 - HD node found: MBR type(2), Signature type(2), Signature(50415353-2A40-4503-
B2F7-474E00EBFBAA), Part #(3), Start LBA(1048576), Num Sectors(1048543)
2024-01-02 17:05:52 - Found DMA Test Partition
2024-01-02 17:05:53 - Successfully obtained Block I/O protocol
2024-01-02 17:05:53 - Successfully obtained Partition Info protocol
2024-01-02 17:05:53 - Revision: 0x1000
2024-01-02 17:05:53 - Type: 0x2
2024-01-02 17:05:53 - System: 0x0
2024-01-02 17:05:53 - GPT - PartitionTypeGUID: EBD0A0A2-B9E5-4433-87C0-68B6B72699C7
2024-01-02 17:05:53 - GPT - UniquePartitionGUID: 50415353-2A40-4503-B2F7-474E00EBFBAA
2024-01-02 17:05:53 - GPT - StartingLBA: 0x100000
2024-01-02 17:05:53 - GPT - EndingLBA: 0x1FFFDE
2024-01-02 17:05:53 - GPT - Attributes: 0x0
2024-01-02 17:05:53 - GPT - PartitionName: DMA Test Partition
2024-01-02 17:05:53 - Media ID: 0x00000000
2024-01-02 17:05:53 - Removable: yes
2024-01-02 17:05:53 - Media Present: yes
2024-01-02 17:05:53 - Logical Part: yes
2024-01-02 17:05:53 - Read-only: no
2024-01-02 17:05:53 - Write cache: no
2024-01-02 17:05:53 - Block size: 512
2024-01-02 17:05:53 - Align: 0
2024-01-02 17:05:53 - Last block: 0xFFFFDE
2024-01-02 17:05:53 - Lowest LBA: 0x0
2024-01-02 17:05:53 - Log blks per phys blk: 0
2024-01-02 17:05:53 - Optimal transfer len: 0
2024-01-02 17:05:54 - [FS0]
2024-01-02 17:05:54 - PciRoot(0x0)/Pci(0x1,0x3)/Pci(0x0,0x0)/USB(0x6,0x0)/HD(1,GPT,50415353-CE04-
4F8F-AC32-6F3693F43704,0x800,0x7F800)
2024-01-02 17:05:54 - File system info buffer size: 38
2024-01-02 17:05:54 - Label: "", Mode: RW, Free space: 246 MB / 254 MB (0%)
2024-01-02 17:05:54 - [FS1]
2024-01-02 17:05:54 - PciRoot(0x0)/Pci(0x1,0x3)/Pci(0x0,0x0)/USB(0x6,0x0)/HD(2,GPT,50415353-6343-
46C0-B68C-69EE6326F32B,0x80000,0x80000)
2024-01-02 17:05:54 - File system info buffer size: 38
2024-01-02 17:05:54 - Label: "", Mode: RW, Free space: 247 MB / 255 MB (0%)
2024-01-02 17:05:54 - [FS2]
2024-01-02 17:05:54 -
PciRoot(0x0)/Pci(0x1,0x3)/Pci(0x0,0x1)/Sata(0x1,0xFFFF,0x0)/HD(2,GPT,93736E34-9B0F-48B4-A436-
083EA0AD672E,0x40800,0x746C6000)
2024-01-02 17:05:54 - File system info buffer size: 50
2024-01-02 17:05:54 - Label: "Platte", Mode: RO, Free space: 0 MB / 953739 MB (0%)
2024-01-02 17:05:54 - [FS3]
2024-01-02 17:05:54 -
PciRoot(0x0)/Pci(0x1,0x3)/Pci(0x0,0x1)/Sata(0x5,0xFFFF,0x0)/HD(1,MBR,0x9DCF6E4A,0x800,0x3A279C05)
2024-01-02 17:05:54 - File system info buffer size: 38
2024-01-02 17:05:54 - Label: "", Mode: RO, Free space: 0 MB / 476403 MB (0%)
2024-01-02 17:05:54 - [FS4]
2024-01-02 17:05:54 -
PciRoot(0x0)/Pci(0x1,0x3)/Pci(0x0,0x1)/Sata(0x5,0xFFFF,0x0)/HD(2,MBR,0x9DCF6E4A,0x3A27A800,0x10A80
0)
2024-01-02 17:05:54 - File system info buffer size: 38
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2024-01-02 17:05:54 - Label: "", Mode: RO, Free space: 0 MB / 532 MB (0%)
2024-01-02 17:05:54 - Disabling watchdog timer (Result: Success)
2024-01-02 17:05:54 - Could not find Simple Network protocol from image device handle
2024-01-02 17:05:55 - Could not get list of handles that support Simple Network protocol: Not Found
2024-01-02 17:05:55 - Found 0 handles that supported Simple Network
2024-01-02 17:05:55 - =====
2024-01-02 17:05:55 - MemTest86 V10.6 Free Build: 3000 (64-bit)
2024-01-02 17:05:55 - =====
2024-01-02 17:05:55 - SMBIOS BIOS INFO Vendor: "American Megatrends Inc.", Version: "P2.00", Release Date: "12/18/2018"
2024-01-02 17:05:55 - SMBIOS SYSTEM INFO Manufacturer: "To Be Filled By O.E.M.", Product: "To Be Filled By O.E.M.", Version: "To Be Filled By O.E.M.", S/N: "To Be Filled By O.E.M.", SKU: "", Family: ""
2024-01-02 17:05:55 - SMBIOS: Found SMBIOS BaseboardInformation (pbLinAddr=0xE95D9, FormattedLen=15, iTotalLen=119)
2024-01-02 17:05:55 - SMBIOS BASEBOARD INFO Manufacturer: "ASRock", Product: "B450M Pro4", Version: "", S/N: "M80-B8025602288", AssetTag: "", LocationInChassis: ""
2024-01-02 17:05:55 - EFI Specifications: 2.60
2024-01-02 17:05:55 - Found blacklist file
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-F42C88C8" (BIOS: "", FLAGS: 1) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-F42386C8" (BIOS: "", FLAGS: 1) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-F4238CC8" (BIOS: "", FLAGS: 8) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-F4208DC8" (BIOS: "", FLAGS: 8) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-27ADBB7B4CEE8E61" (BIOS: "", FLAGS: 20) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-7BA5B2DFE22DDD8C" (BIOS: "", FLAGS: 4) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-7BA5B2D9E42DDD94" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-AF89B6D9451A490B" (BIOS: "", FLAGS: 4) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-CFF7D910A743CAAF" (BIOS: "", FLAGS: 4) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-AA95B1DDAB278B95" (BIOS: "", FLAGS: 4) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Mac-27AD2F918AE68F61" (BIOS: "", FLAGS: 40) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "80AF" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Z97MX-Gaming 5" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Z170MX-Gaming 5" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Z170X-Gaming 3" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Z170X-Gaming 7" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Z170X-Gaming GT" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Z170X-UD3-CF" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "Z170-HD3P" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:55 - [BLACKLIST] Adding "990FXA-UD3" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "990FX Extreme3" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "970A-DS3P" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X99-Ultra Gaming-CF" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "M5A97 R2.0" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "M5A97 EVO R2.0" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "M5A99FX PRO R2.0" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "M5A99X EVO R2.0" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X99-A" (BIOS: "3701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X99-A/USB 3.1" (BIOS: "3701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X99-A II" (BIOS: "1701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X99-A" (BIOS: "1701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "Sabertooth X99" (BIOS: "3701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "SABERTOOTH 990FX R2.0" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "SABERTOOTH 990FX R3.0" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "SABERTOOTH 990FX/GEN3 R2.0" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "STRIX X99 GAMING" (BIOS: "1701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X99-DELUXE" (BIOS: "3701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X99-DELUXE II" (BIOS: "1701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "RAMPAGE V EXTREME" (BIOS: "3701", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "RAMPAGE V EDITION 10" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "MAXIMUS VIII RANGER" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "P9X79 WS" (BIOS: "", FLAGS: 40) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "P9X79-E WS" (BIOS: "", FLAGS: 40) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "Z9PE-D8 WS" (BIOS: "", FLAGS: 40) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "Z10PE-D8 WS" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X9DRW" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X9DRW-3LN4F+/X9DRW-3TF+" (BIOS: "", FLAGS: 2) to blacklist
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2024-01-02 17:05:56 - [BLACKLIST] Adding "X9DR3-F" (BIOS: "", FLAGS: 40) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X9SRL-F" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:56 - [BLACKLIST] Adding "X9DRD-iF" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "X9SRA/X9SRA-3" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "X9DRL-7F" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "151-BE-E097" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "131-HE-E095" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "131-HE-E095-KR" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "151-HE-E999-KR" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "PRIME B350-PLUS" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "PRIME X370-PRO" (BIOS: "", FLAGS: 4) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "CROSSHAIR VI HERO" (BIOS: "1602", FLAGS: 4) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "CROSSHAIR V FORMULA-Z" (BIOS: "", FLAGS: 4) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "ROG STRIX X370-F GAMING" (BIOS: "4012", FLAGS: 4) to
blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "OEMSH Product" (BIOS: "", FLAGS: 8) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "OEMAL Product" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "OEMCT Product" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "OEMWY Product" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "LNVNB161216" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "GA-990FX-GAMING" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "X399 SLI PLUS (MS-7B09)" (BIOS: "", FLAGS: 2) to
blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "X570 I AORUS PRO WIFI" (BIOS: "F32", FLAGS: 2) to
blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "MS-17EK" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "0F685W" (BIOS: "", FLAGS: 80) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "00V5FJ" (BIOS: "", FLAGS: 100) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "ProLiant DL325 Gen10" (BIOS: "", FLAGS: 4) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "ProLiant DL385 Gen10" (BIOS: "", FLAGS: 4) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "ProLiant DL385 Gen10 Plus" (BIOS: "", FLAGS: 4) to
blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "06CDVY" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "Surface Book" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "Surface Book 2" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:57 - [BLACKLIST] Adding "Surface Pro" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "Surface Pro 7" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "Surface Pro" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "Surface Laptop 4" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "Surface Laptop" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "MACH-WX9-PCB" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "MACH-WX9" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "8873" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "0P4NHH" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "XPS 17 9710" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "XPS 15 9510" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "01V4T3" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "0VX936" (BIOS: "", FLAGS: 10) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "H12DST-B" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] Adding "H12DSi-NT6" (BIOS: "", FLAGS: 2) to blacklist
2024-01-02 17:05:58 - [BLACKLIST] 93 boards added to blacklist
2024-01-02 17:05:58 - Console Control protocol workaround enabled
2024-01-02 17:05:58 - Number of console modes: 4
2024-01-02 17:05:58 - Mode 0: 80 x 25
2024-01-02 17:05:58 - Mode 1: 128 x 40
2024-01-02 17:05:58 - Mode 2: 100 x 31
2024-01-02 17:05:58 - Mode 3: 200 x 63
2024-01-02 17:05:58 - Console attribute: 15
2024-01-02 17:05:58 - Initializing localization strings
2024-01-02 17:05:58 - Language en-US is supported
2024-01-02 17:05:58 - GetGlyph failed for character 0x00C6 (Not Found)
2024-01-02 17:05:58 - Language fr-FR is not supported
2024-01-02 17:05:58 - Language it-IT is supported
2024-01-02 17:05:58 - Language es-AR is supported
2024-01-02 17:05:58 - GetGlyph failed for character 0x00C3 (Not Found)
2024-01-02 17:05:58 - Language pt-BR is not supported
2024-01-02 17:05:59 - GetGlyph failed for character 0x00EF (Not Found)
2024-01-02 17:05:59 - Language ca-ES is not supported
2024-01-02 17:05:59 - Language de-DE is supported
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2024-01-02 17:05:59 - GetGlyph failed for character 0x010C (Not Found)
2024-01-02 17:05:59 - Language cs-CZ is not supported
2024-01-02 17:05:59 - GetGlyph failed for character 0x0104 (Not Found)
2024-01-02 17:05:59 - Language pl-PL is not supported
2024-01-02 17:05:59 - Language ru-RU is supported
2024-01-02 17:05:59 - GetGlyph failed for character 0x3041 (Not Found)
2024-01-02 17:05:59 - Language ja-JP is not supported
2024-01-02 17:05:59 - GetGlyph failed for character 0x4E0F (Not Found)
2024-01-02 17:05:59 - Language zh-CN is not supported
2024-01-02 17:05:59 - GetGlyph failed for character 0xF944 (Not Found)
2024-01-02 17:05:59 - Language zh-HK is not supported
2024-01-02 17:05:59 - Font does not support all languages. Installing unicode font...
2024-01-02 17:05:59 - InitFont - Font header version: 1.1
2024-01-02 17:05:59 - InitFont - Number of narrow glyphs: 7110
2024-01-02 17:05:59 - InitFont - Number of wide glyphs: 49976
2024-01-02 17:05:59 - Language en-US is supported
2024-01-02 17:05:59 - Language fr-FR is supported
2024-01-02 17:05:59 - Language it-IT is supported
2024-01-02 17:05:59 - Language es-AR is supported
2024-01-02 17:05:59 - Language pt-BR is supported
2024-01-02 17:05:59 - Language ca-ES is supported
2024-01-02 17:05:59 - Language de-DE is supported
2024-01-02 17:06:00 - Language cs-CZ is supported
2024-01-02 17:06:00 - Language pl-PL is supported
2024-01-02 17:06:00 - Language ru-RU is supported
2024-01-02 17:06:00 - Language ja-JP is supported
2024-01-02 17:06:00 - Language zh-CN is supported
2024-01-02 17:06:00 - Language zh-HK is supported
2024-01-02 17:06:00 - iMS support is NOT available
2024-01-02 17:06:00 - Initializing screen for graphics
2024-01-02 17:06:00 - Number of graphics modes: 5
2024-01-02 17:06:00 - Mode 0: 640 x 480
2024-01-02 17:06:00 - Mode 1: 800 x 600
2024-01-02 17:06:00 - Mode 2: 1024 x 768
2024-01-02 17:06:00 - Mode 3: 1280 x 1024
2024-01-02 17:06:00 - Mode 4: 1600 x 1200 [Current]
2024-01-02 17:06:00 - Getting CPUID
2024-01-02 17:06:00 - Getting cache size
2024-01-02 17:06:00 - get_cache_size - Vendor ID: AuthenticAMD Brand ID: AMD Ryzen 5 2600X Six-Core Processor
2024-01-02 17:06:00 - CPU CorePPack: 12 (0x0000400B)
2024-01-02 17:06:00 - L1 instruction cache size: 12 x 32KB
2024-01-02 17:06:00 - L1 data cache size: 12 x 64KB
2024-01-02 17:06:00 - L2 cache size: 12 x 512KB
2024-01-02 17:06:00 - L3 cache size: 1 x 16384KB
2024-01-02 17:06:00 - Measuring CPU/cache/mem speed
2024-01-02 17:06:00 - hpet_init - Found RSDP. Version: 2
2024-01-02 17:06:00 - hpet_init - Found XSDT. Entry Count: 19
2024-01-02 17:06:00 - hpet_init - Found ACPI table: FACP Version: 6
2024-01-02 17:06:00 - hpet_init - Found ACPI table: APIC Version: 3
2024-01-02 17:06:00 - hpet_init - Found ACPI table: FPDT Version: 1
2024-01-02 17:06:00 - hpet_init - Found ACPI table: FIDT Version: 1
2024-01-02 17:06:01 - hpet_init - Found ACPI table: SSDT Version: 2
2024-01-02 17:06:01 - hpet_init - Found ACPI table: SSDT Version: 1
2024-01-02 17:06:01 - hpet_init - Found ACPI table: CRAT Version: 1
2024-01-02 17:06:01 - hpet_init - Found ACPI table: CDIT Version: 1
2024-01-02 17:06:01 - hpet_init - Found ACPI table: SSDT Version: 1
2024-01-02 17:06:01 - hpet_init - Found ACPI table: MCFG Version: 1
2024-01-02 17:06:01 - hpet_init - Found ACPI table: AAFI Version: 1
2024-01-02 17:06:01 - hpet_init - Found ACPI table: HPET Version: 1
2024-01-02 17:06:01 - hpet_init - HPET base address: 0xFED00000
2024-01-02 17:06:01 - hpet_init - HPET gen cap: 0x429B17E10228201 (Period: 69841278fs)
2024-01-02 17:06:01 - hpet_init - HPET gen cfg: 0x1
2024-01-02 17:06:01 - hpet_init - main counter value (1): 296691703
2024-01-02 17:06:01 - hpet_init - main counter value (2): 297141201
2024-01-02 17:06:01 - cpuspeed_hpet - rdtsc instruction supported: 1
2024-01-02 17:06:01 - cpuspeed_hpet - HPET counter start: 298036734, HPET counter end: 298752643
2024-01-02 17:06:01 - cpuspeed_hpet - start TSC: 98049530088, end TSC: 98230309308 (difference: 180779220)

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2024-01-02 17:06:01 - cpu_cache_speed - (Attempt 1) clock cycle (ms): 3615584
2024-01-02 17:06:01 - cpuspeed_hpet - rdtsc instruction supported: 1
2024-01-02 17:06:01 - cpuspeed_hpet - HPET counter start: 300535439, HPET counter end: 301251348
2024-01-02 17:06:01 - cpuspeed_hpet - start TSC: 98680462632, end TSC: 98861234004 (difference:
180771372)
2024-01-02 17:06:01 - cpu_cache_speed - (Attempt 2) clock cycle (ms): 3615427
2024-01-02 17:06:01 - cpuspeed_hpet - rdtsc instruction supported: 1
2024-01-02 17:06:01 - cpuspeed_hpet - HPET counter start: 303184968, HPET counter end: 303900877
2024-01-02 17:06:01 - cpuspeed_hpet - start TSC: 99349467336, end TSC: 99530248536 (difference:
180781200)
2024-01-02 17:06:01 - cpu_cache_speed - (Attempt 3) clock cycle (ms): 3615624
2024-01-02 17:06:01 - cpu_cache_speed - Using average of last 3 samples (3615624, 3615427,
3615584)
2024-01-02 17:06:02 - cpu_cache_speed - CPU speed: 3615.5MHz
2024-01-02 17:06:02 - cpu_cache_speed - measuring L1 cache speed at 0xD904D000 (64 KB)
2024-01-02 17:06:02 - cpu_cache_speed - L1 cache speed: 95870 MB/s
2024-01-02 17:06:02 - cpu_cache_speed - measuring L2 cache speed at 0xD8FDD000 (512 KB)
2024-01-02 17:06:02 - cpu_cache_speed - L2 cache speed: 72143 MB/s
2024-01-02 17:06:02 - cpu_cache_speed - measuring L3 cache speed at 0xD656F000 (16384 KB)
2024-01-02 17:06:02 - cpu_cache_speed - L3 cache speed: 28714 MB/s
2024-01-02 17:06:02 - get_mem_speed - measuring mem speed at 0xCC579000 (135936 KB)
2024-01-02 17:06:03 - get_mem_speed - mem speed: 14943 MB/s
2024-01-02 17:06:03 - memory latency - allocate memory
2024-01-02 17:06:03 - memory latency - randomize range
2024-01-02 17:06:03 - memory latency - build linked list
2024-01-02 17:06:03 - memory latency - start test
2024-01-02 17:06:04 - memory latency - test completed (loops=4, rpl=2097152)
2024-01-02 17:06:04 - cpu_cache_speed - Memory latency: 77.247 ns
2024-01-02 17:06:04 - Retrieving CPU MSR data
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - CPU AMD Ryzen 5 2600X Six-Core Processor :
17h.8h
2024-01-02 17:06:04 - IsAMDTurboCoreSupported - CPUID[0x80000007][EDX] = 00006799
2024-01-02 17:06:04 - HWCR register found. CPB enable/disable functionality supported
2024-01-02 17:06:04 - CPB is: enabled
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - MSR[0xC0010292] = 0000000104000052
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - MSR Max Pstates/#boosted states: 8, -1
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - MSR[C0010064] = 80000000490E8890
2024-01-02 17:06:04 - GetAMDMult - CPUDid_P = 8, CPUFid_P = 144
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - Multiplier_P[0] = 36000
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - MSR[C0010065] = 8000000048154880
2024-01-02 17:06:04 - GetAMDMult - CPUDid_P = 8, CPUFid_P = 128
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - Multiplier_P[1] = 32000
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - MSR[C0010066] = 80000000459C8C84
2024-01-02 17:06:04 - GetAMDMult - CPUDid_P = 12, CPUFid_P = 132
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - Multiplier_P[2] = 22000
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - MSR[C0010067] = 0000000000000000
2024-01-02 17:06:04 - GetAMDMult - CPUDid_P = 0, CPUFid_P = 0
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - Multiplier_P[3] = 0
2024-01-02 17:06:04 - GetAMDFamilyMSRInfo - MSR[C0010068] = 0000000000000000
2024-01-02 17:06:04 - GetAMDMult - CPUDid_P = 0, CPUFid_P = 0
2024-01-02 17:06:05 - GetAMDFamilyMSRInfo - Multiplier_P[4] = 0
2024-01-02 17:06:05 - GetAMDFamilyMSRInfo - MSR[C0010069] = 0000000000000000
2024-01-02 17:06:05 - GetAMDMult - CPUDid_P = 0, CPUFid_P = 0
2024-01-02 17:06:05 - GetAMDFamilyMSRInfo - Multiplier_P[5] = 0
2024-01-02 17:06:05 - GetAMDFamilyMSRInfo - MSR[C001006A] = 0000000000000000
2024-01-02 17:06:05 - GetAMDMult - CPUDid_P = 0, CPUFid_P = 0
2024-01-02 17:06:05 - GetAMDFamilyMSRInfo - Multiplier_P[6] = 0
2024-01-02 17:06:05 - GetAMDFamilyMSRInfo - MSR[C001006B] = 0000000000000000
2024-01-02 17:06:05 - GetAMDMult - CPUDid_P = 0, CPUFid_P = 0
2024-01-02 17:06:05 - GetAMDFamilyMSRInfo - Multiplier_P[7] = 0
2024-01-02 17:06:05 - EnableAMDCPB - MSR[0xC0010015] = 0000000009000011
2024-01-02 17:06:05 - EnableAMDCPB - New MSR[0xC0010015] = 0000000009000011
2024-01-02 17:06:05 - AMD MSR enable CPD: MSR[00000000C0010062] = 0x0000000000000000
2024-01-02 17:06:05 - AMD Set PStateCtl: 0x0000000000000000
2024-01-02 17:06:05 - Get_MPERF_APERF - CPUID[0x6][ECX] = 00000001
2024-01-02 17:06:05 - Get_MPERF_APERF - high perf counter freq = 3615545000
2024-01-02 17:06:06 - MSR 0xC0010293: 00000000000808AA
2024-01-02 17:06:06 - GetAMDMult - CPUDid_P = 8, CPUFid_P = 170
2024-01-02 17:06:06 - MSR 0xC0010063: 0000000000000000
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2024-01-02 17:06:06 - MSR 0xC0010062: 0000000000000000
2024-01-02 17:06:06 - MSR 0xC0010061: 0000000000000020 (CurPState=0, MaxPState=2)
2024-01-02 17:06:06 - Get_MPERF_APERF - time in ns = 959576
2024-01-02 17:06:06 - Get_MPERF_APERF - FreqMPERF in KHz = 3616297, FreqAPERF in KHz = 4268636
2024-01-02 17:06:08 - MSR 0xC0010293: 000000000000808AA
2024-01-02 17:06:08 - GetAMDMult - CPUDid_P = 8, CPUFid_P = 170
2024-01-02 17:06:08 - MSR 0xC0010063: 0000000000000000
2024-01-02 17:06:08 - MSR 0xC0010062: 0000000000000000
2024-01-02 17:06:08 - MSR 0xC0010061: 0000000000000020 (CurPState=0, MaxPState=2)
2024-01-02 17:06:08 - Get_MPERF_APERF - time in ns = 908098
2024-01-02 17:06:08 - Get_MPERF_APERF - FreqMPERF in KHz = 3616142, FreqAPERF in KHz = 4268646
2024-01-02 17:06:11 - MSR 0xC0010293: 000000000000848AA
2024-01-02 17:06:11 - GetAMDMult - CPUDid_P = 8, CPUFid_P = 170
2024-01-02 17:06:11 - MSR 0xC0010063: 0000000000000000
2024-01-02 17:06:11 - MSR 0xC0010062: 0000000000000000
2024-01-02 17:06:11 - MSR 0xC0010061: 0000000000000020 (CurPState=0, MaxPState=2)
2024-01-02 17:06:11 - Get_MPERF_APERF - time in ns = 929685
2024-01-02 17:06:11 - Get_MPERF_APERF - FreqMPERF in KHz = 3616128, FreqAPERF in KHz = 4268645
2024-01-02 17:06:11 - GetAMDFamilyMSRInfo - APERF = 4268646, MPERF = 3616297, Mult = 42500
2024-01-02 17:06:11 - GetAMDFamilyMSRInfo - BaseClock = 10043
2024-01-02 17:06:11 - GetAMDFamilyMSRInfo - Turbo Core supported
2024-01-02 17:06:11 - DisableAMDCPB - MSR[0xC0010015] = 0000000009000011
2024-01-02 17:06:11 - DisableAMDCPB - New MSR[0xC0010015] = 000000000B000011
2024-01-02 17:06:11 - AMD MSR disable CPD: MSR[00000000C0010062] = 0x0000000000000000
2024-01-02 17:06:11 - AMD Set PStateCtl: 0x0000000000000000
2024-01-02 17:06:11 - Get_MPERF_APERF - CPUID[0x6][ECX] = 00000001
2024-01-02 17:06:12 - Get_MPERF_APERF - high perf counter freq = 3615545000
2024-01-02 17:06:13 - MSR 0xC0010293: 0000000000140890
2024-01-02 17:06:13 - GetAMDMult - CPUDid_P = 8, CPUFid_P = 144
2024-01-02 17:06:13 - MSR 0xC0010063: 0000000000000000
2024-01-02 17:06:13 - MSR 0xC0010062: 0000000000000000
2024-01-02 17:06:13 - MSR 0xC0010061: 0000000000000020 (CurPState=0, MaxPState=2)
2024-01-02 17:06:13 - Get_MPERF_APERF - time in ns = 1141252
2024-01-02 17:06:13 - Get_MPERF_APERF - FreqMPERF in KHz = 3616112, FreqAPERF in KHz = 3615786
2024-01-02 17:06:14 - MSR 0xC0010293: 0000000000140890
2024-01-02 17:06:14 - GetAMDMult - CPUDid_P = 8, CPUFid_P = 144
2024-01-02 17:06:14 - MSR 0xC0010063: 0000000000000000
2024-01-02 17:06:14 - MSR 0xC0010062: 0000000000000000
2024-01-02 17:06:14 - MSR 0xC0010061: 0000000000000020 (CurPState=0, MaxPState=2)
2024-01-02 17:06:14 - Get_MPERF_APERF - time in ns = 1041333
2024-01-02 17:06:14 - Get_MPERF_APERF - FreqMPERF in KHz = 3616168, FreqAPERF in KHz = 3615795
2024-01-02 17:06:15 - MSR 0xC0010293: 0000000000140890
2024-01-02 17:06:15 - GetAMDMult - CPUDid_P = 8, CPUFid_P = 144
2024-01-02 17:06:15 - MSR 0xC0010063: 0000000000000000
2024-01-02 17:06:15 - MSR 0xC0010062: 0000000000000000
2024-01-02 17:06:15 - MSR 0xC0010061: 0000000000000020 (CurPState=0, MaxPState=2)
2024-01-02 17:06:15 - Get_MPERF_APERF - time in ns = 1119227
2024-01-02 17:06:15 - Get_MPERF_APERF - FreqMPERF in KHz = 3616124, FreqAPERF in KHz = 3615777
2024-01-02 17:06:15 - GetAMDFamilyMSRInfo - (CPB disabled) APERF = 3615795, MPERF = 3616168, Mult = 36000
2024-01-02 17:06:15 - GetAMDFamilyMSRInfo - (CPB disabled) BaseClock = 10043
2024-01-02 17:06:15 - GetAMDFamilyMSRInfo - CPUSpeedTurboTheoreticalMax = 3615480
2024-01-02 17:06:15 - GetAMDFamilyMSRInfo - (Before sanity check) Freq: 3615795KHz (Ratio: 36000, ExtClk: 10043KHz), Turbo: 4268646KHz (Ratio: 42500, ExtClk: 10043KHz) (Theoretical Max: 4268646KHz)
2024-01-02 17:06:15 - GetAMDFamilyMSRInfo - Freq: 3615795, 36000, 10043, Turbo: 4268646, 42500, 10043 (4268646)
2024-01-02 17:06:15 - Getting memory size
2024-01-02 17:06:15 - 0x000000000000 - 0x000000007FFF (32KB) {Boot Services Code}

2024-01-02 17:06:15 - 0x000000008000 - 0x00000003EFFF (220KB) {Free Memory}

2024-01-02 17:06:15 - 0x00000003F000 - 0x00000003FFFF (4KB) {Boot Services Data}

2024-01-02 17:06:15 - 0x000000040000 - 0x00000009FFFF (384KB) {Boot Services Code}

2024-01-02 17:06:16 - 0x000000100000 - 0x000000FFFFFF (15MB) {Free Memory}

2024-01-02 17:06:16 - 0x000001000000 - 0x00000101FFFF (128KB) {Boot Services Data}
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2024-01-02 17:06:16 - 0x000001020000 - 0x000009CFFFFF (140MB) {Free Memory}
2024-01-02 17:06:16 - 0x000009D00000 - 0x000009FFFFFF (3MB) {Reserved Memory}
2024-01-02 17:06:16 - 0x00000A000000 - 0x00000A1FFFFFF (2MB) {Free Memory}
2024-01-02 17:06:16 - 0x00000A200000 - 0x00000A209FFF (40KB) {ACPI Non-volatile}
2024-01-02 17:06:16 - 0x00000A20A000 - 0x00000AFFFFFF (13MB) {Free Memory}
2024-01-02 17:06:16 - 0x00000B000000 - 0x00000B01FFFF (128KB) {Reserved Memory}
2024-01-02 17:06:16 - 0x00000B020000 - 0x00000D4A38FFF (3226MB) {Free Memory}
2024-01-02 17:06:16 - 0x00000D4A39000 - 0x00000D4B96FFF (1400KB) {Loader Code}
2024-01-02 17:06:16 - 0x00000D4B97000 - 0x00000D756EFFF (41MB) {Free Memory}
2024-01-02 17:06:16 - 0x00000D756F000 - 0x00000D77AEFFF (2MB) {Boot Services Data}
2024-01-02 17:06:16 - 0x00000D77AF000 - 0x00000D7E47FFF (6MB) {Free Memory}
2024-01-02 17:06:16 - 0x00000D7E48000 - 0x00000D8EAEFFF (16MB) {Boot Services Data}
2024-01-02 17:06:16 - 0x00000D8EAF000 - 0x00000D905AFF (1712KB) {Free Memory}
2024-01-02 17:06:16 - 0x00000D905B000 - 0x00000DA76AFF (23MB) {Boot Services Data}
2024-01-02 17:06:16 - 0x00000DA76B000 - 0x00000DA8C2FFF (1376KB) {Free Memory}
2024-01-02 17:06:16 - 0x00000DA8C3000 - 0x00000DAE25FFF (5MB) {Boot Services Code}
2024-01-02 17:06:16 - 0x00000DAE26000 - 0x00000DC311FFF (20MB) {Reserved Memory}
2024-01-02 17:06:16 - 0x00000DC312000 - 0x00000DC406FFF (980KB) {Free Memory}
2024-01-02 17:06:16 - 0x00000DC407000 - 0x00000DC7DEFFF (3MB) {ACPI Non-volatile}
2024-01-02 17:06:16 - 0x00000DC7DF000 - 0x00000DD1B4FFF (9MB) {Runtime Services Data}
2024-01-02 17:06:16 - 0x00000DD1B5000 - 0x00000DD247FFF (588KB) {Runtime Services Code}
2024-01-02 17:06:16 - 0x00000DD248000 - 0x00000DD3CAFFF (1548KB) {Boot Services Data}
2024-01-02 17:06:16 - 0x00000DD3CB000 - 0x00000DD3EBFFF (132KB) {Boot Services Code}
2024-01-02 17:06:16 - 0x00000DD3EC000 - 0x00000DD41EFFF (204KB) {Boot Services Data}
2024-01-02 17:06:16 - 0x00000DD41F000 - 0x00000DD433FFF (84KB) {Boot Services Code}
2024-01-02 17:06:16 - 0x00000DD434000 - 0x00000DE8C9FFF (20MB) {Boot Services Data}
2024-01-02 17:06:16 - 0x00000DE8CA000 - 0x00000DE8D0FFF (28KB) {Boot Services Code}
2024-01-02 17:06:17 - 0x00000DE8D1000 - 0x00000DE8E8FFF (96KB) {Boot Services Data}
2024-01-02 17:06:17 - 0x00000DE8E9000 - 0x00000DE8E9FFF (4KB) {Boot Services Code}
2024-01-02 17:06:17 - 0x00000DE8EA000 - 0x00000DE907FFF (120KB) {Boot Services Data}
2024-01-02 17:06:17 - 0x00000DE908000 - 0x00000DE917FFF (64KB) {Boot Services Code}
2024-01-02 17:06:17 - 0x00000DE918000 - 0x00000DE938FFF (132KB) {Boot Services Data}
2024-01-02 17:06:17 - 0x00000DE939000 - 0x00000DE93FFFF (28KB) {Boot Services Code}
2024-01-02 17:06:17 - 0x00000DE940000 - 0x00000DEFF8FFF (6MB) {Boot Services Data}

2024-01-02 17:06:17 - 0x0000DEFF9000 - 0x0000DEFFFFFF (28KB) {Boot Services Code}
2024-01-02 17:06:17 - 0x000100000000 - 0x00041F37FFFF (12787MB) {Free Memory}
2024-01-02 17:06:17 - 0x0000000A0000 - 0x0000000FFFFFFF (384KB) {Reserved Memory}
2024-01-02 17:06:17 - 0x0000DF000000 - 0x0000DFFFFFFF (16MB) {Reserved Memory}
2024-01-02 17:06:17 - 0x0000F8000000 - 0x0000FBFFFFFF (64MB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FD100000 - 0x0000FDFFFFFF (15MB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FEA00000 - 0x0000FEA0FFFF (64KB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FEB80000 - 0x0000FEC01FFF (520KB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FEC10000 - 0x0000FEC10FFF (4KB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FEC30000 - 0x0000FEC30FFF (4KB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FED00000 - 0x0000FED00FFF (4KB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FED40000 - 0x0000FED44FFF (20KB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FED80000 - 0x0000FED8FFFF (64KB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FEE00000 - 0x0000FEEFFFFFFF (1024KB) {OS Memory-mapped IO}
2024-01-02 17:06:17 - 0x0000FF000000 - 0x0000FFFFFFFF (16MB) {OS Memory-mapped IO}
2024-01-02 17:06:17 -
2024-01-02 17:06:17 - Available Memory: 0x3F6E10000 (15GB)
2024-01-02 17:06:17 - Reserved Memory: 0xE61A000 (230MB)
2024-01-02 17:06:17 - mem_size - Number of entries: 55
2024-01-02 17:06:17 - mem_size - Max address: 0x41F380000
2024-01-02 17:06:17 - mem_size - Total memory size (17120305152 bytes)
2024-01-02 17:06:17 - Getting memory SPD info
2024-01-02 17:06:17 - Attempting to enable any disabled SMBus controllers...
2024-01-02 17:06:18 - Attempting to enable SMBUS:
2024-01-02 17:06:18 - Searching PCI for SMBus Controller
2024-01-02 17:06:18 - PM DECODEEN[0]=00000077
2024-01-02 17:06:18 - PM DECODEEN[1]=0000000B
2024-01-02 17:06:18 - PM DECODEEN[2]=00000007 (smbus0sel=0)
2024-01-02 17:06:18 - MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:18 - {00:14:00} SMBus Configuration Registers:
2024-01-02 17:06:18 - 00: 790B1022 02200403 0C050059 00800000
2024-01-02 17:06:18 - 10: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - 20: 00000000 00000000 00000000 790B1849
2024-01-02 17:06:18 - 30: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - 40: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - 50: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - 60: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - 70: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - 80: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - 90: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - A0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - B0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - C0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - D0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - E0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - F0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:18 - Found SMBus device: VID:1022 DID:790B Bus:00 Dev:14 Fun:00 IO Add:0B00 IO
(2) Add:0000 MMIO Add:0 PCI Add:{00:00:00:0000} Rev:59 [AMD Hudson-3 SMBus]
2024-01-02 17:06:19 - Finished searching PCI for SMBus Controller
2024-01-02 17:06:19 - Found 1 SMBus controllers
2024-01-02 17:06:19 - [AMD Hudson-3 SMBus Bus:00 Dev:14 Fun:00 Add:0B00] Looking for SPD

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2024-01-02 17:06:21 - 28 00 00 00 00 00 00 00 00 00 00 D2 8E 8E E0 AC
2024-01-02 17:06:21 - 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
2024-01-02 17:06:21 - 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
2024-01-02 17:06:21 - 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
2024-01-02 17:06:21 - 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
2024-01-02 17:06:21 - 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
2024-01-02 17:06:21 - Module Manufacturer's Specific Data raw bytes:
2024-01-02 17:06:21 - 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
2024-01-02 17:06:22 - 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
2024-01-02 17:06:22 - DIMM#1 (Channel -1, Slot -1) serial number: 00000000
2024-01-02 17:06:22 - Previous MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:22 - Writing MMIO PM DECODEEN: E30F0B77 (smbus0sel=1)
2024-01-02 17:06:22 - Restoring MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:22 - Previous MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:22 - Writing MMIO PM DECODEEN: E3170B77 (smbus0sel=2)
2024-01-02 17:06:22 - Restoring MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:22 - Previous MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:22 - Writing MMIO PM DECODEEN: E31F0B77 (smbus0sel=3)
2024-01-02 17:06:22 - Restoring MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:22 - Enumerating PCI bus...
2024-01-02 17:06:22 - Scanning PCI for devices:
2024-01-02 17:06:22 - VID:1022 DID:1450 Bus:00 Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1451 Bus:00 Dev:00 Fun:02 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1452 Bus:00 Dev:01 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1453 Bus:00 Dev:01 Fun:03 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1452 Bus:00 Dev:02 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1452 Bus:00 Dev:03 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1453 Bus:00 Dev:03 Fun:01 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1452 Bus:00 Dev:04 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1452 Bus:00 Dev:07 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1454 Bus:00 Dev:07 Fun:01 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1452 Bus:00 Dev:08 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - VID:1022 DID:1454 Bus:00 Dev:08 Fun:01 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:22 - PM DECODEEN[0]=00000077
2024-01-02 17:06:22 - PM DECODEEN[1]=0000000B
2024-01-02 17:06:22 - PM DECODEEN[2]=00000007 (smbus0sel=0)
2024-01-02 17:06:22 - MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:23 - {00:14:00} SMBus Configuration Registers:
2024-01-02 17:06:23 - 00: 790B1022 02200403 0C050059 00800000
2024-01-02 17:06:23 - 10: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - 20: 00000000 00000000 00000000 790B1849
2024-01-02 17:06:23 - 30: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - 40: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - 50: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - 60: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - 70: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - 80: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - 90: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - A0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - B0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - C0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - D0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - E0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - F0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:23 - VID:1022 DID:790B Bus:00 Dev:14 Fun:00 IO Add:0B00 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:59 [AMD Hudson-3 SMBus]
2024-01-02 17:06:23 - VID:1022 DID:790E Bus:00 Dev:14 Fun:03 IO Add:0000 IO (2) Add:0000 MMIO

Add:0 PCI Add:{00:00:00:0000} Rev:51 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:1460 Bus:00 Dev:18 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:1461 Bus:00 Dev:18 Fun:01 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:1462 Bus:00 Dev:18 Fun:02 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:1463 Bus:00 Dev:18 Fun:03 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:1464 Bus:00 Dev:18 Fun:04 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:1465 Bus:00 Dev:18 Fun:05 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:1466 Bus:00 Dev:18 Fun:06 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:1467 Bus:00 Dev:18 Fun:07 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:23 - VID:1022 DID:43D5 Bus:03 Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:01 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:43C8 Bus:03 Dev:00 Fun:01 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:01 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:43C6 Bus:03 Dev:00 Fun:02 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:01 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:43C7 Bus:1D Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:01 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:43C7 Bus:1D Dev:01 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:01 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:43C7 Bus:1D Dev:04 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:01 [AMD Unknown]
2024-01-02 17:06:24 - VID:10EC DID:8168 Bus:1F Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:15 [Unknown Unknown]
2024-01-02 17:06:24 - VID:1022 DID:1470 Bus:23 Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:C3 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:1471 Bus:24 Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:24 - VID:1002 DID:687F Bus:25 Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:C3 [ATI Unknown]
2024-01-02 17:06:24 - VID:1002 DID:AAF8 Bus:25 Dev:00 Fun:01 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [ATI Unknown]
2024-01-02 17:06:24 - VID:1022 DID:145A Bus:26 Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:1456 Bus:26 Dev:00 Fun:02 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:145F Bus:26 Dev:00 Fun:03 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:1455 Bus:27 Dev:00 Fun:00 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:7901 Bus:27 Dev:00 Fun:02 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:51 [AMD Unknown]
2024-01-02 17:06:24 - VID:1022 DID:1457 Bus:27 Dev:00 Fun:03 IO Add:0000 IO (2) Add:0000 MMIO
Add:0 PCI Add:{00:00:00:0000} Rev:00 [AMD Unknown]
2024-01-02 17:06:24 - Finished searching PCI for SMBus Controller
2024-01-02 17:06:24 - Getting SMBIOS Memory Device info...
2024-01-02 17:06:24 - Found 4 Memory Devices
2024-01-02 17:06:24 - DEBUG: Get SMB mem device Struct 0
2024-01-02 17:06:24 - DEBUG: Get SMB mem device Struct Get strings (FormattedLen=40, iTotalLen=55)
2024-01-02 17:06:24 - DEBUG: Get SMB mem device Struct: String area len=45
2024-01-02 17:06:24 - DEBUG: Get SMB mem device Struct: parsing v2.3+ strings
2024-01-02 17:06:24 - DEBUG: Get SMB mem device Struct: parsing v3.2+ strings
2024-01-02 17:06:24 - DEBUG: Get SMB mem device Struct end
2024-01-02 17:06:24 - [Slot 0] TotalWidth: 65535, DataWidth: 65535, Size: 0, FormFactor: 2,
DeviceSet: 0, MemoryType: 2, Speed: 0, Attributes: 0, ExtendedSize: 0, ConfiguredMemoryClockSpeed:
0, MinimumVoltage: 0, MaximumVoltage: 0, ConfiguredVoltage: 0
2024-01-02 17:06:25 - [Slot 0] DeviceLocator: DIMM 0, BankLocator: P0 CHANNEL A, Manufacturer:
Unknown, S/N: Unknown, AssetTag: , PartNumber: Unknown
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct 1
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct Get strings (FormattedLen=40, iTotalLen=5F)
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: String area len=55
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: parsing v2.3+ strings

2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: parsing v3.2+ strings
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct end
2024-01-02 17:06:25 - [Slot 1] TotalWidth: 64, DataWidth: 64, Size: 8192, FormFactor: 9, DeviceSet: 0, MemoryType: 26, Speed: 2134, Attributes: 1, ExtendedSize: 0, ConfiguredMemoryClockSpeed: 1067, MinimumVoltage: 1200, MaximumVoltage: 1200, ConfiguredVoltage: 1200
2024-01-02 17:06:25 - [Slot 1] DeviceLocator: DIMM 1, BankLocator: P0 CHANNEL A, Manufacturer: Unknown, S/N: 00000000, AssetTag: , PartNumber: F4-3000C16-8GISB
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct 2
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct Get strings (FormattedLen=40, iTotalLen=55)
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: String area len=45
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: parsing v2.3+ strings
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: parsing v3.2+ strings
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct end
2024-01-02 17:06:25 - [Slot 2] TotalWidth: 65535, DataWidth: 65535, Size: 0, FormFactor: 2, DeviceSet: 0, MemoryType: 2, Speed: 0, Attributes: 0, ExtendedSize: 0, ConfiguredMemoryClockSpeed: 0, MinimumVoltage: 0, MaximumVoltage: 0, ConfiguredVoltage: 0
2024-01-02 17:06:25 - [Slot 2] DeviceLocator: DIMM 0, BankLocator: P0 CHANNEL B, Manufacturer: Unknown, S/N: Unknown, AssetTag: , PartNumber: Unknown
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct 3
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct Get strings (FormattedLen=40, iTotalLen=5F)
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: String area len=55
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: parsing v2.3+ strings
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct: parsing v3.2+ strings
2024-01-02 17:06:25 - DEBUG: Get SMB mem device Struct end
2024-01-02 17:06:25 - [Slot 3] TotalWidth: 64, DataWidth: 64, Size: 8192, FormFactor: 9, DeviceSet: 0, MemoryType: 26, Speed: 2134, Attributes: 1, ExtendedSize: 0, ConfiguredMemoryClockSpeed: 1067, MinimumVoltage: 1200, MaximumVoltage: 1200, ConfiguredVoltage: 1200
2024-01-02 17:06:25 - [Slot 3] DeviceLocator: DIMM 1, BankLocator: P0 CHANNEL B, Manufacturer: Unknown, S/N: 00000000, AssetTag: , PartNumber: F4-3000C16-8GISB
2024-01-02 17:06:25 - Number of non-empty SMBIOS memory devices found: 2
2024-01-02 17:06:25 - [SPD 0] Comparing SMBIOS S/N (00000000) with SPD S/N (00000000)
2024-01-02 17:06:25 - [SPD 0] SMBIOS S/N = 00000000
2024-01-02 17:06:26 - [SPD 0] Found match with SMBIOS 1
2024-01-02 17:06:26 - [SPD 1] Comparing SMBIOS S/N (00000000) with SPD S/N (00000000)
2024-01-02 17:06:26 - [SPD 1] SMBIOS S/N = 00000000
2024-01-02 17:06:26 - [SPD 1] Found match with SMBIOS 3
2024-01-02 17:06:26 - Attempting to enable any disabled TSOD controllers...
2024-01-02 17:06:26 - Attempting to enable SMBUS:
2024-01-02 17:06:26 - Searching PCI for TSOD Controller
2024-01-02 17:06:26 - PM DECODEEN[0]=00000077
2024-01-02 17:06:26 - PM DECODEEN[1]=0000000B
2024-01-02 17:06:26 - PM DECODEEN[2]=00000007 (smbus0sel=0)
2024-01-02 17:06:26 - MMIO PM DECODEEN: E3070B77 (smbus0sel=0)
2024-01-02 17:06:26 - {00:14:00} SMBus Configuration Registers:
2024-01-02 17:06:26 - 00: 790B1022 02200403 0C050059 00800000
2024-01-02 17:06:26 - 10: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - 20: 00000000 00000000 00000000 790B1849
2024-01-02 17:06:26 - 30: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - 40: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - 50: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - 60: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - 70: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - 80: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - 90: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - A0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - B0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - C0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - D0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - E0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:26 - F0: 00000000 00000000 00000000 00000000
2024-01-02 17:06:27 - Found TSOD device: VID:1022 DID:790B Bus:00 Dev:14 Fun:00 IO Add:0B00 IO (2) Add:0000 MMIO Add:0 Rev:59 [AMD Hudson-3 SMBus]
2024-01-02 17:06:27 - Finished searching PCI for SMBus Controller
2024-01-02 17:06:27 - Expanding TSOD info array (0 => 16)
2024-01-02 17:06:27 - Parsing command line: ""
2024-01-02 17:06:27 - Successfully located the PI MpService protocol.
2024-01-02 17:06:27 - BSP is Proc 0

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2024-01-02 17:06:27 - This platform has 12 logical processors of which 12 are enabled.
2024-01-02 17:06:27 - ProcID   Enabled   Type    Status   Pkg    Core   Thread  Hyperthread?
2024-01-02 17:06:27 - -----
2024-01-02 17:06:27 - Get Proc Info Proc #0
2024-01-02 17:06:27 - 00000000      Y      BSP  00000007      0      0      0      N
2024-01-02 17:06:27 - Get Proc Info Proc #1
2024-01-02 17:06:27 - 00000001      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:27 - Get Proc Info Proc #2
2024-01-02 17:06:27 - 00000002      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:27 - Get Proc Info Proc #3
2024-01-02 17:06:27 - 00000003      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:27 - Get Proc Info Proc #4
2024-01-02 17:06:27 - 00000004      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:27 - Get Proc Info Proc #5
2024-01-02 17:06:27 - 00000005      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:27 - Get Proc Info Proc #6
2024-01-02 17:06:27 - 00000008      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:27 - Get Proc Info Proc #7
2024-01-02 17:06:27 - 00000009      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:27 - Get Proc Info Proc #8
2024-01-02 17:06:28 - 0000000A      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:28 - Get Proc Info Proc #9
2024-01-02 17:06:28 - 0000000B      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:28 - Get Proc Info Proc #10
2024-01-02 17:06:28 - 0000000C      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:28 - Get Proc Info Proc #11
2024-01-02 17:06:28 - 0000000D      Y      AP   00000006      0      0      0      N
2024-01-02 17:06:28 - Package      ProcNum
2024-01-02 17:06:28 -      0          0
2024-01-02 17:06:28 - This platform has 12 logical processors of which 12 are enabled.
2024-01-02 17:06:28 - Number of hyperthreads detected: 0 (Total threads: 12)
2024-01-02 17:06:28 - Disabling all hyperthreads
2024-01-02 17:06:28 - Testing MP support
2024-01-02 17:06:28 - MPSupportTestMPServices - AP dispatch test
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#1
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#2
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#3
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#4
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#5
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#6
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#7
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#8
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#9
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#10
2024-01-02 17:06:28 - MPSupportTestMPServices - Starting AP#11
2024-01-02 17:06:28 - MPSupportTestMPServices - Waiting for AP's to complete execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #1 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #2 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #3 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #4 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #5 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #6 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #7 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #8 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #9 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #10 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - CPU #11 finished execution
2024-01-02 17:06:29 - MPSupportTestMPServices - AP dispatch test complete
2024-01-02 17:06:30 - MP test passed. Setting default CPU mode to PARALLEL
2024-01-02 17:06:30 - Getting memory controller info
2024-01-02 17:06:30 - find_mem_controller - found AMD Ryzen (0h-fh) (1022:1462) at 0-24-2
2024-01-02 17:06:30 - AMD Ryzen chipset init
2024-01-02 17:06:30 - CfgAddressCnt1 = 00000000 (SecBusNum=00)
2024-01-02 17:06:30 - CPUID[0x80000007]:EBX[31:0] = 0000001B (ScalableMCA=1)
2024-01-02 17:06:30 - MCG_CAP=00000000000000117 (Count=23)
2024-01-02 17:06:30 - SdpCtrl[0]=B0408082 (SdpInit=1)
2024-01-02 17:06:30 - [MC0] DramConfiguration=00000120
2024-01-02 17:06:30 - [MC0] DebugMisc=000000F8
2024-01-02 17:06:30 - [MC0] DramTiming1=0F0F240F
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2024-01-02 17:06:30 - [MC0] DramTiming2=000F0033
2024-01-02 17:06:30 - [MC0] DramTiming3=08000604
2024-01-02 17:06:30 - [MC0] DramTiming4=00000017
2024-01-02 17:06:30 - [MC0] DramTiming5=0008030B
2024-01-02 17:06:30 - [MC0] DramTiming6=00000010
2024-01-02 17:06:30 - [MC0] DramTiming7=00000000
2024-01-02 17:06:30 - [MC0] DramTiming8=03010404
2024-01-02 17:06:30 - [MC0] DramTiming9=03010606
2024-01-02 17:06:30 - [MC0] DramTiming10=00000A00
2024-01-02 17:06:30 - [MC0] DramTiming11=08320080
2024-01-02 17:06:30 - [MC0] DramTiming12=0000207C
2024-01-02 17:06:30 - [MC0] DramTiming13=18101808
2024-01-02 17:06:30 - [MC0] DramTiming14=03000180
2024-01-02 17:06:30 - [MC0] DramTiming15=00002024
2024-01-02 17:06:30 - [MC0] DramTiming16=00000000
2024-01-02 17:06:30 - [MC0] DramTiming17=3FFF0006
2024-01-02 17:06:30 - [MC0] DramTiming18=00000000
2024-01-02 17:06:30 - [MC0] DramTiming19=00000000
2024-01-02 17:06:31 - [MC0] DramTiming20=00060000
2024-01-02 17:06:31 - [MC0] DramTiming21=06040007
2024-01-02 17:06:31 - [MC0] DramTiming22=0216060A
2024-01-02 17:06:31 - [MC0] DramTiming23=22002A2A
2024-01-02 17:06:31 - UmcCap[0]=0x0001FE2C
2024-01-02 17:06:31 - UmcCapHi[0]=0x00000000
2024-01-02 17:06:31 - EccChipKillCap = 0 and EccEnabled = 0 for ch 0
2024-01-02 17:06:31 - SdpCtrl[1]=B0408082 (SdpInit=1)
2024-01-02 17:06:31 - [MC1] DramConfiguration=00000120
2024-01-02 17:06:31 - [MC1] DebugMisc=000000F8
2024-01-02 17:06:31 - [MC1] DramTiming1=0F0F240F
2024-01-02 17:06:31 - [MC1] DramTiming2=000F0033
2024-01-02 17:06:31 - [MC1] DramTiming3=08000604
2024-01-02 17:06:31 - [MC1] DramTiming4=00000017
2024-01-02 17:06:31 - [MC1] DramTiming5=0008030B
2024-01-02 17:06:31 - [MC1] DramTiming6=00000010
2024-01-02 17:06:31 - [MC1] DramTiming7=00000000
2024-01-02 17:06:31 - [MC1] DramTiming8=03010404
2024-01-02 17:06:31 - [MC1] DramTiming9=03010606
2024-01-02 17:06:31 - [MC1] DramTiming10=00000A00
2024-01-02 17:06:31 - [MC1] DramTiming11=08320080
2024-01-02 17:06:31 - [MC1] DramTiming12=0000207C
2024-01-02 17:06:31 - [MC1] DramTiming13=18101808
2024-01-02 17:06:31 - [MC1] DramTiming14=03000180
2024-01-02 17:06:31 - [MC1] DramTiming15=00002024
2024-01-02 17:06:31 - [MC1] DramTiming16=00000000
2024-01-02 17:06:31 - [MC1] DramTiming17=3FFF0006
2024-01-02 17:06:31 - [MC1] DramTiming18=00000000
2024-01-02 17:06:31 - [MC1] DramTiming19=00000000
2024-01-02 17:06:31 - [MC1] DramTiming20=00060000
2024-01-02 17:06:32 - [MC1] DramTiming21=06040007
2024-01-02 17:06:32 - [MC1] DramTiming22=0214060A
2024-01-02 17:06:32 - [MC1] DramTiming23=22002A2A
2024-01-02 17:06:32 - UmcCap[1]=0x0001FE2C
2024-01-02 17:06:32 - UmcCapHi[1]=0x00000000
2024-01-02 17:06:32 - EccChipKillCap = 0 and EccEnabled = 0 for ch 1
2024-01-02 17:06:32 - find_mem_controller - AMD Ryzen (0h-fh) (1022:1462) at 0-24-2
2024-01-02 17:06:32 - find_mem_controller - AMD Ryzen (0h-fh) (Ch mode: 2, ECC mode: {detect: no,
correct: no, scrub: no, chipkill: no, IB ECC: no})
2024-01-02 17:06:32 - ECC polling disabled
2024-01-02 17:06:32 - Applying configurations
2024-01-02 17:06:32 - [CONFIG] This platform has 12 logical processors of which 12 are enabled.
2024-01-02 17:06:32 - [CONFIG] ECC POLL cannot be enabled as ECC is not available
2024-01-02 17:06:32 - [CONFIG] SPDREPORTBYTELO must be less than or equal to SPDREPORTBYTEHI
2024-01-02 17:06:32 - Applying configurations complete
2024-01-02 17:06:32 - Console size = 80 x 25
2024-01-02 17:06:32 - Checking for graphics mode support
2024-01-02 17:06:32 - Graphics mode available
2024-01-02 17:06:32 - Enabling graphics mode
2024-01-02 17:06:32 - Get screen size
2024-01-02 17:06:32 - Current screen size: 1600 x 1200
```

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2024-01-02 17:06:32 - Screen size = 1600 x 1200
2024-01-02 17:06:32 - Char width=8 height=19
2024-01-02 17:06:32 - Loading images
2024-01-02 17:06:32 - Show splash screen
2024-01-02 17:06:32 - Splash screen start time: 211051142028 (clk=3615545, ConIn=DAAF02B8,
WaitForKey=D96E6118)
2024-01-02 17:06:42 - Splash screen countdown timer expired
2024-01-02 17:06:43 - Exit splash screen
2024-01-02 17:06:43 - Initializing spin lock (Align=64)
2024-01-02 17:06:43 - *** TEST SESSION - 2024-01-02 17:06:43 ***
2024-01-02 17:06:43 - CPU selection mode = 1
2024-01-02 17:06:43 - poll_timings_ryzen - [MC0] DramConfiguration=00000120
2024-01-02 17:06:43 - poll_timings_ryzen - [MC0] DebugMisc=000000F8
2024-01-02 17:06:43 - poll_timings_ryzen - [MC1] DramConfiguration=00000120
2024-01-02 17:06:43 - poll_timings_ryzen - [MC1] DebugMisc=000000F8
2024-01-02 17:06:43 - ReadMemoryRanges - Available Pages = 4156787
2024-01-02 17:06:43 - Locking all memory ranges first...
2024-01-02 17:06:43 - Skipping memory range 0x8000 - 0x3F000 (220KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0x100000 - 0x1000000 (15360KB). Range too small.
2024-01-02 17:06:43 - Memory range locked: 0x1020000 - 0x9D00000 (16482892KB of available memory
left)
2024-01-02 17:06:43 - Skipping memory range 0xA000000 - 0xA200000 (2048KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0xA20A000 - 0xB000000 (14296KB). Range too small.
2024-01-02 17:06:43 - Memory range locked: 0xB020000 - 0xD4A39000 (13179368KB of available memory
left)
2024-01-02 17:06:43 - Memory range locked: 0xD4B97000 - 0xD756F000 (13136520KB of available memory
left)
2024-01-02 17:06:43 - Skipping memory range 0xD77AF000 - 0xD7E48000 (6756KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0xD8EAF000 - 0xD8FA1000 (968KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0xD8FB2000 - 0xD8FBF000 (52KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0xD8FC9000 - 0xD8FD3000 (40KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0xD8FEB000 - 0xD8FF0000 (20KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0xD9016000 - 0xD9017000 (4KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0xDA76B000 - 0xDA8C3000 (1376KB). Range too small.
2024-01-02 17:06:43 - Skipping memory range 0xDC312000 - 0xDC407000 (980KB). Range too small.
2024-01-02 17:06:43 - Memory range locked: 0x100000000 - 0x41F380000 (42120KB of available memory
left)
2024-01-02 17:06:43 - All memory ranges successfully locked
2024-01-02 17:06:43 - Starting pass #1 (of 4)
2024-01-02 17:06:44 - DIMM0 temperature: 0.000C
2024-01-02 17:06:44 - DIMM1 temperature: 0.000C
2024-01-02 17:06:44 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:06:44 - Current CPU temperature: 42C
2024-01-02 17:06:44 - Running test #0 (Test 0 [Address test, walking ones, 1 CPU])
2024-01-02 17:06:44 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:06:44 - MtSupportRunAllTests - Start time: 1142 ms
2024-01-02 17:06:44 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:06:44 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:06:44 - Start memory range test (0x0 - 0x41F380000)
2024-01-02 17:06:44 - MtSupportRunAllTests - Test execution time: 0.409s (Test 0 cumulative error
count: 0, buffer full count: 0)
2024-01-02 17:06:44 - Running test #1 (Test 1 [Address test, own address, 1 CPU])
2024-01-02 17:06:44 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:06:44 - MtSupportRunAllTests - Start time: 1766 ms
2024-01-02 17:06:44 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:06:44 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:06:45 - Start memory range test (0x0 - 0x41F380000)
2024-01-02 17:06:49 - MtSupportRunAllTests - Test execution time: 4.376s (Test 1 cumulative error
count: 0, buffer full count: 0)
2024-01-02 17:06:49 - Running test #2 (Test 2 [Address test, own address])
2024-01-02 17:06:49 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:06:49 - MtSupportRunAllTests - Start time: 6334 ms
2024-01-02 17:06:49 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:06:49 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:06:49 - Start memory range test (0x0 - 0x41F380000)
2024-01-02 17:06:51 - MtSupportRunAllTests - Test execution time: 2.011s (Test 2 cumulative error
count: 0, buffer full count: 0)
2024-01-02 17:06:51 - Running test #3 (Test 3 [Moving inversions, ones & zeroes])
2024-01-02 17:06:51 - MtSupportRunAllTests - Setting random seed to 0x50415353
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2024-01-02 17:06:51 - MtSupportRunAllTests - Start time: 8539 ms
2024-01-02 17:06:51 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:06:51 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:06:51 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:07:06 - MtSupportRunAllTests - Test execution time: 14.196s (Test 3 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:07:06 - Running test #4 (Test 4 [Moving inversions, 8-bit pattern])
2024-01-02 17:07:06 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:07:06 - MtSupportRunAllTests - Start time: 22927 ms
2024-01-02 17:07:06 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:07:06 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:07:06 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:07:44 - DIMM0 temperature: 0.000C
2024-01-02 17:07:44 - DIMM1 temperature: 0.000C
2024-01-02 17:07:44 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:07:44 - Current CPU temperature: 46C
2024-01-02 17:08:14 - MtSupportRunAllTests - Test execution time: 68.040s (Test 4 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:08:14 - Running test #5 (Test 5 [Moving inversions, random pattern])
2024-01-02 17:08:14 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:08:14 - MtSupportRunAllTests - Start time: 91159 ms
2024-01-02 17:08:14 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:08:14 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:08:15 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:08:45 - DIMM0 temperature: 0.000C
2024-01-02 17:08:45 - DIMM1 temperature: 0.000C
2024-01-02 17:08:45 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:08:45 - Current CPU temperature: 48C
2024-01-02 17:09:26 - MtSupportRunAllTests - Test execution time: 70.835s (Test 5 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:09:26 - Running test #6 (Test 6 [Block move, 64-byte blocks])
2024-01-02 17:09:26 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:09:26 - MtSupportRunAllTests - Start time: 162230 ms
2024-01-02 17:09:26 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:09:26 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:09:26 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:09:45 - DIMM0 temperature: 0.000C
2024-01-02 17:09:45 - DIMM1 temperature: 0.000C
2024-01-02 17:09:45 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:09:46 - Current CPU temperature: 44C
2024-01-02 17:10:17 - MtSupportRunAllTests - Test execution time: 50.509s (Test 6 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:10:17 - Running test #7 (Test 7 [Moving inversions, 32-bit pattern])
2024-01-02 17:10:17 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:10:17 - MtSupportRunAllTests - Start time: 212926 ms
2024-01-02 17:10:17 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:10:17 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:10:17 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:10:46 - DIMM0 temperature: 0.000C
2024-01-02 17:10:46 - DIMM1 temperature: 0.000C
2024-01-02 17:10:46 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:10:46 - Current CPU temperature: 50C
2024-01-02 17:11:47 - DIMM0 temperature: 0.000C
2024-01-02 17:11:47 - DIMM1 temperature: 0.000C
2024-01-02 17:11:47 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:11:47 - Current CPU temperature: 51C
2024-01-02 17:12:47 - DIMM0 temperature: 0.000C
2024-01-02 17:12:47 - DIMM1 temperature: 0.000C
2024-01-02 17:12:47 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:12:47 - Current CPU temperature: 52C
2024-01-02 17:13:48 - DIMM0 temperature: 0.000C
2024-01-02 17:13:48 - DIMM1 temperature: 0.000C
2024-01-02 17:13:48 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:13:48 - Current CPU temperature: 52C
2024-01-02 17:14:49 - DIMM0 temperature: 0.000C
2024-01-02 17:14:49 - DIMM1 temperature: 0.000C
2024-01-02 17:14:49 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:14:49 - Current CPU temperature: 52C
2024-01-02 17:14:51 - MtSupportRunAllTests - Test execution time: 272.019s (Test 7 cumulative


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error count: 0, buffer full count: 0)
2024-01-02 17:14:51 - Running test #8 (Test 8 [Random number sequence])
2024-01-02 17:14:51 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:14:51 - MtSupportRunAllTests - Start time: 485173 ms
2024-01-02 17:14:51 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:14:51 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:14:51 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:15:26 - MtSupportRunAllTests - Test execution time: 34.926s (Test 8 cumulative error
count: 0, buffer full count: 0)
2024-01-02 17:15:26 - Running test #9 (Test 9 [Modulo 20, ones & zeros])
2024-01-02 17:15:26 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:15:26 - MtSupportRunAllTests - Start time: 520302 ms
2024-01-02 17:15:26 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:15:26 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:15:26 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:15:49 - DIMM0 temperature: 0.000C
2024-01-02 17:15:49 - DIMM1 temperature: 0.000C
2024-01-02 17:15:49 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:15:49 - Current CPU temperature: 52C
2024-01-02 17:16:50 - DIMM0 temperature: 0.000C
2024-01-02 17:16:50 - DIMM1 temperature: 0.000C
2024-01-02 17:16:50 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:16:50 - Current CPU temperature: 51C
2024-01-02 17:17:50 - DIMM0 temperature: 0.000C
2024-01-02 17:17:50 - DIMM1 temperature: 0.000C
2024-01-02 17:17:50 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:17:50 - Current CPU temperature: 51C
2024-01-02 17:18:16 - MtSupportRunAllTests - Test execution time: 169.001s (Test 9 cumulative
error count: 0, buffer full count: 0)
2024-01-02 17:18:16 - Running test #10 (Test 10 [Bit fade test, 2 patterns, 1 CPU])
2024-01-02 17:18:16 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:18:16 - MtSupportRunAllTests - Start time: 689494 ms
2024-01-02 17:18:16 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:18:16 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:18:17 - Start bit fade test (0x0 - 0x41F38000)
2024-01-02 17:18:17 - Starting Stage 0, Pattern=00000000
2024-01-02 17:18:19 - Finished Stage 0, Pattern=00000000
2024-01-02 17:18:19 - Sleep start time: 692480
2024-01-02 17:18:19 - Sleep time: 300000
2024-01-02 17:18:19 - Test 10 - Sleeping (299 seconds remaining)
2024-01-02 17:18:50 - Slept for 30 seconds
2024-01-02 17:18:51 - DIMM0 temperature: 0.000C
2024-01-02 17:18:51 - DIMM1 temperature: 0.000C
2024-01-02 17:18:51 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:18:51 - Current CPU temperature: 35C
2024-01-02 17:19:20 - Slept for 60 seconds
2024-01-02 17:19:50 - Slept for 90 seconds
2024-01-02 17:19:52 - DIMM0 temperature: 0.000C
2024-01-02 17:19:52 - DIMM1 temperature: 0.000C
2024-01-02 17:19:52 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:19:52 - Current CPU temperature: 32C
2024-01-02 17:20:20 - Slept for 120 seconds
2024-01-02 17:20:50 - Slept for 150 seconds
2024-01-02 17:20:52 - DIMM0 temperature: 0.000C
2024-01-02 17:20:52 - DIMM1 temperature: 0.000C
2024-01-02 17:20:52 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:20:52 - Current CPU temperature: 31C
2024-01-02 17:21:21 - Slept for 180 seconds
2024-01-02 17:21:51 - Slept for 210 seconds
2024-01-02 17:21:53 - DIMM0 temperature: 0.000C
2024-01-02 17:21:53 - DIMM1 temperature: 0.000C
2024-01-02 17:21:53 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:21:53 - Current CPU temperature: 31C
2024-01-02 17:22:21 - Slept for 240 seconds
2024-01-02 17:22:51 - Slept for 270 seconds
2024-01-02 17:22:53 - DIMM0 temperature: 0.000C
2024-01-02 17:22:53 - DIMM1 temperature: 0.000C
2024-01-02 17:22:53 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:22:53 - Current CPU temperature: 30C
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2024-01-02 17:23:21 - Starting Stage 1, Pattern=00000000
2024-01-02 17:23:25 - Finished Stage 1, Pattern=00000000
2024-01-02 17:23:25 - Starting Stage 0, Pattern=FFFFFFF
2024-01-02 17:23:27 - Finished Stage 0, Pattern=FFFFFFF
2024-01-02 17:23:27 - Sleep start time: 998646
2024-01-02 17:23:28 - Sleep time: 300000
2024-01-02 17:23:28 - Test 10 - Sleeping (299 seconds remaining)
2024-01-02 17:23:54 - DIMM0 temperature: 0.000C
2024-01-02 17:23:54 - DIMM1 temperature: 0.000C
2024-01-02 17:23:54 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:23:54 - Current CPU temperature: 31C
2024-01-02 17:23:58 - Slept for 30 seconds
2024-01-02 17:24:28 - Slept for 60 seconds
2024-01-02 17:24:55 - DIMM0 temperature: 0.000C
2024-01-02 17:24:55 - DIMM1 temperature: 0.000C
2024-01-02 17:24:55 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:24:55 - Current CPU temperature: 30C
2024-01-02 17:24:58 - Slept for 90 seconds
2024-01-02 17:25:28 - Slept for 120 seconds
2024-01-02 17:25:55 - DIMM0 temperature: 0.000C
2024-01-02 17:25:55 - DIMM1 temperature: 0.000C
2024-01-02 17:25:55 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:25:55 - Current CPU temperature: 30C
2024-01-02 17:25:59 - Slept for 150 seconds
2024-01-02 17:26:29 - Slept for 180 seconds
2024-01-02 17:26:56 - DIMM0 temperature: 0.000C
2024-01-02 17:26:56 - DIMM1 temperature: 0.000C
2024-01-02 17:26:56 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:26:56 - Current CPU temperature: 30C
2024-01-02 17:26:59 - Slept for 210 seconds
2024-01-02 17:27:29 - Slept for 240 seconds
2024-01-02 17:27:56 - DIMM0 temperature: 0.000C
2024-01-02 17:27:57 - DIMM1 temperature: 0.000C
2024-01-02 17:27:57 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:27:57 - Current CPU temperature: 30C
2024-01-02 17:28:00 - Slept for 270 seconds
2024-01-02 17:28:29 - Starting Stage 1, Pattern=FFFFFFF
2024-01-02 17:28:33 - Finished Stage 1, Pattern=FFFFFFF
2024-01-02 17:28:33 - MtSupportRunAllTests - Test execution time: 612.476s (Test 10 cumulative
error count: 0, buffer full count: 0)
2024-01-02 17:28:33 - Running test #13 (Test 13 [Hammer test])
2024-01-02 17:28:33 - MtSupportRunAllTests - Setting random seed to 0x50415353
2024-01-02 17:28:33 - MtSupportRunAllTests - Start time: 1302215 ms
2024-01-02 17:28:33 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:28:33 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:28:33 - Start double-sided hammer test (0x0 - 0x41F38000, Step size: 0x1000000)
2024-01-02 17:28:33 - Using random pattern
2024-01-02 17:28:33 - # of segments to hammer: 7 (SegSize=2048MB, SkipSize=0MB)
2024-01-02 17:28:57 - DIMM0 temperature: 0.000C
2024-01-02 17:28:57 - DIMM1 temperature: 0.000C
2024-01-02 17:28:57 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:28:57 - Current CPU temperature: 41C
2024-01-02 17:29:03 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0xB020000 -
0x8B020000]
2024-01-02 17:29:23 - RunHammerTest - Running hammer test on 2 CPUs (589MB each) [0x8B020000 -
0xD4A39000]
2024-01-02 17:29:58 - DIMM0 temperature: 0.000C
2024-01-02 17:29:58 - DIMM1 temperature: 0.000C
2024-01-02 17:29:58 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:29:58 - Current CPU temperature: 41C
2024-01-02 17:30:04 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x100000000 -
0x1800000000]
2024-01-02 17:30:24 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x1800000000 -
0x2000000000]
2024-01-02 17:30:45 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x2000000000 -
0x2800000000]
2024-01-02 17:30:58 - DIMM0 temperature: 0.000C
2024-01-02 17:30:58 - DIMM1 temperature: 0.000C
2024-01-02 17:30:58 - iTrueTmp != 0 && usingBlanketOffset == true
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2024-01-02 17:30:58 - Current CPU temperature: 41C
2024-01-02 17:31:06 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x280000000 - 0x300000000]
2024-01-02 17:31:26 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x300000000 - 0x380000000]
2024-01-02 17:31:47 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x380000000 - 0x400000000]
2024-01-02 17:31:59 - DIMM0 temperature: 0.000C
2024-01-02 17:31:59 - DIMM1 temperature: 0.000C
2024-01-02 17:31:59 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:31:59 - Current CPU temperature: 41C
2024-01-02 17:32:18 - MtSupportRunAllTests - Test execution time: 223.494s (Test 13 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:32:18 - Finished pass #1 (of 4) (Cumulative error count: 0, buffer full count: 0)
2024-01-02 17:32:18 - Starting pass #2 (of 4)
2024-01-02 17:32:18 - Running test #0 (Test 0 [Address test, walking ones, 1 CPU])
2024-01-02 17:32:18 - MtSupportRunAllTests - Setting random seed to 0x41AB9CB4
2024-01-02 17:32:18 - MtSupportRunAllTests - Start time: 1526000 ms
2024-01-02 17:32:18 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:32:18 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:32:18 - Start memory range test (0x0 - 0x41F380000)
2024-01-02 17:32:19 - MtSupportRunAllTests - Test execution time: 0.413s (Test 0 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:32:19 - Running test #1 (Test 1 [Address test, own address, 1 CPU])
2024-01-02 17:32:19 - MtSupportRunAllTests - Setting random seed to 0xC2E51154
2024-01-02 17:32:19 - MtSupportRunAllTests - Start time: 1526600 ms
2024-01-02 17:32:19 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:32:19 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:32:19 - Start memory range test (0x0 - 0x41F380000)
2024-01-02 17:32:23 - MtSupportRunAllTests - Test execution time: 4.381s (Test 1 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:32:23 - Running test #2 (Test 2 [Address test, own address])
2024-01-02 17:32:23 - MtSupportRunAllTests - Setting random seed to 0x9ADD0AC0
2024-01-02 17:32:23 - MtSupportRunAllTests - Start time: 1531166 ms
2024-01-02 17:32:23 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:32:23 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:32:23 - Start memory range test (0x0 - 0x41F380000)
2024-01-02 17:32:25 - MtSupportRunAllTests - Test execution time: 2.010s (Test 2 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:32:25 - Running test #3 (Test 3 [Moving inversions, ones & zeroes])
2024-01-02 17:32:26 - MtSupportRunAllTests - Setting random seed to 0x78C8E004
2024-01-02 17:32:26 - MtSupportRunAllTests - Start time: 1533384 ms
2024-01-02 17:32:26 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:32:26 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:32:26 - Start memory range test (0x0 - 0x41F380000)
2024-01-02 17:32:59 - DIMM0 temperature: 0.000C
2024-01-02 17:32:59 - DIMM1 temperature: 0.000C
2024-01-02 17:32:59 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:32:59 - Current CPU temperature: 47C
2024-01-02 17:33:03 - MtSupportRunAllTests - Test execution time: 36.754s (Test 3 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:33:03 - Running test #4 (Test 4 [Moving inversions, 8-bit pattern])
2024-01-02 17:33:03 - MtSupportRunAllTests - Setting random seed to 0x9AD57FE0
2024-01-02 17:33:03 - MtSupportRunAllTests - Start time: 1570367 ms
2024-01-02 17:33:03 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:33:03 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:33:03 - Start memory range test (0x0 - 0x41F380000)
2024-01-02 17:34:00 - DIMM0 temperature: 0.000C
2024-01-02 17:34:00 - DIMM1 temperature: 0.000C
2024-01-02 17:34:00 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:34:00 - Current CPU temperature: 48C
2024-01-02 17:35:01 - DIMM0 temperature: 0.000C
2024-01-02 17:35:01 - DIMM1 temperature: 0.000C
2024-01-02 17:35:01 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:35:01 - Current CPU temperature: 49C
2024-01-02 17:35:42 - MtSupportRunAllTests - Test execution time: 158.589s (Test 4 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:35:42 - Running test #5 (Test 5 [Moving inversions, random pattern])
2024-01-02 17:35:43 - MtSupportRunAllTests - Setting random seed to 0x464DA10C

2024-01-02 17:35:43 - MtSupportRunAllTests - Start time: 1729156 ms
2024-01-02 17:35:43 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:35:43 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:35:43 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:36:01 - DIMM0 temperature: 0.000C
2024-01-02 17:36:01 - DIMM1 temperature: 0.000C
2024-01-02 17:36:01 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:36:01 - Current CPU temperature: 49C
2024-01-02 17:37:02 - DIMM0 temperature: 0.000C
2024-01-02 17:37:02 - DIMM1 temperature: 0.000C
2024-01-02 17:37:02 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:37:02 - Current CPU temperature: 49C
2024-01-02 17:38:03 - DIMM0 temperature: 0.000C
2024-01-02 17:38:03 - DIMM1 temperature: 0.000C
2024-01-02 17:38:03 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:38:03 - Current CPU temperature: 49C
2024-01-02 17:39:03 - DIMM0 temperature: 0.000C
2024-01-02 17:39:03 - DIMM1 temperature: 0.000C
2024-01-02 17:39:03 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:39:03 - Current CPU temperature: 49C
2024-01-02 17:39:17 - MtSupportRunAllTests - Test execution time: 212.667s (Test 5 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:39:17 - Running test #6 (Test 6 [Block move, 64-byte blocks])
2024-01-02 17:39:17 - MtSupportRunAllTests - Setting random seed to 0x76397A54
2024-01-02 17:39:17 - MtSupportRunAllTests - Start time: 1942016 ms
2024-01-02 17:39:17 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:39:17 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:39:17 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:40:04 - DIMM0 temperature: 0.000C
2024-01-02 17:40:04 - DIMM1 temperature: 0.000C
2024-01-02 17:40:04 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:40:04 - Current CPU temperature: 44C
2024-01-02 17:41:05 - DIMM0 temperature: 0.000C
2024-01-02 17:41:05 - DIMM1 temperature: 0.000C
2024-01-02 17:41:05 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:41:05 - Current CPU temperature: 43C
2024-01-02 17:41:45 - MtSupportRunAllTests - Test execution time: 147.380s (Test 6 cumulative error count: 0, buffer full count: 0)
2024-01-02 17:41:45 - Running test #7 (Test 7 [Moving inversions, 32-bit pattern])
2024-01-02 17:41:45 - MtSupportRunAllTests - Setting random seed to 0xB609E508
2024-01-02 17:41:45 - MtSupportRunAllTests - Start time: 2089622 ms
2024-01-02 17:41:45 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:41:45 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:41:45 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:42:05 - DIMM0 temperature: 0.000C
2024-01-02 17:42:05 - DIMM1 temperature: 0.000C
2024-01-02 17:42:05 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:42:06 - Current CPU temperature: 51C
2024-01-02 17:43:06 - DIMM0 temperature: 0.000C
2024-01-02 17:43:06 - DIMM1 temperature: 0.000C
2024-01-02 17:43:06 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:43:06 - Current CPU temperature: 52C
2024-01-02 17:44:07 - DIMM0 temperature: 0.000C
2024-01-02 17:44:07 - DIMM1 temperature: 0.000C
2024-01-02 17:44:07 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:44:07 - Current CPU temperature: 53C
2024-01-02 17:45:08 - DIMM0 temperature: 0.000C
2024-01-02 17:45:08 - DIMM1 temperature: 0.000C
2024-01-02 17:45:08 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:45:08 - Current CPU temperature: 53C
2024-01-02 17:46:09 - DIMM0 temperature: 0.000C
2024-01-02 17:46:09 - DIMM1 temperature: 0.000C
2024-01-02 17:46:09 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:46:09 - Current CPU temperature: 53C
2024-01-02 17:47:09 - DIMM0 temperature: 0.000C
2024-01-02 17:47:09 - DIMM1 temperature: 0.000C
2024-01-02 17:47:09 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:47:09 - Current CPU temperature: 53C
2024-01-02 17:48:10 - DIMM0 temperature: 0.000C

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2024-01-02 17:48:10 - DIMM1 temperature: 0.000C
2024-01-02 17:48:10 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:48:10 - Current CPU temperature: 53C
2024-01-02 17:49:11 - DIMM0 temperature: 0.000C
2024-01-02 17:49:11 - DIMM1 temperature: 0.000C
2024-01-02 17:49:11 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:49:11 - Current CPU temperature: 53C
2024-01-02 17:50:12 - DIMM0 temperature: 0.000C
2024-01-02 17:50:12 - DIMM1 temperature: 0.000C
2024-01-02 17:50:12 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:50:12 - Current CPU temperature: 53C
2024-01-02 17:51:12 - DIMM0 temperature: 0.000C
2024-01-02 17:51:12 - DIMM1 temperature: 0.000C
2024-01-02 17:51:13 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:51:13 - Current CPU temperature: 53C
2024-01-02 17:52:13 - DIMM0 temperature: 0.000C
2024-01-02 17:52:13 - DIMM1 temperature: 0.000C
2024-01-02 17:52:13 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:52:13 - Current CPU temperature: 53C
2024-01-02 17:52:25 - MtSupportRunAllTests - Test execution time: 635.852s (Test 7 cumulative
error count: 0, buffer full count: 0)
2024-01-02 17:52:25 - Running test #8 (Test 8 [Random number sequence])
2024-01-02 17:52:25 - MtSupportRunAllTests - Setting random seed to 0x3BB3F884
2024-01-02 17:52:25 - MtSupportRunAllTests - Start time: 2725778 ms
2024-01-02 17:52:25 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:52:25 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:52:25 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:53:14 - DIMM0 temperature: 0.000C
2024-01-02 17:53:14 - DIMM1 temperature: 0.000C
2024-01-02 17:53:14 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:53:14 - Current CPU temperature: 64C
2024-01-02 17:54:10 - MtSupportRunAllTests - Test execution time: 104.166s (Test 8 cumulative
error count: 0, buffer full count: 0)
2024-01-02 17:54:10 - Running test #9 (Test 9 [Modulo 20, ones & zeros])
2024-01-02 17:54:10 - MtSupportRunAllTests - Setting random seed to 0x246C7178
2024-01-02 17:54:10 - MtSupportRunAllTests - Start time: 2830206 ms
2024-01-02 17:54:10 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 17:54:11 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 17:54:11 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 17:54:14 - DIMM0 temperature: 0.000C
2024-01-02 17:54:14 - DIMM1 temperature: 0.000C
2024-01-02 17:54:15 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:54:15 - Current CPU temperature: 58C
2024-01-02 17:55:15 - DIMM0 temperature: 0.000C
2024-01-02 17:55:15 - DIMM1 temperature: 0.000C
2024-01-02 17:55:15 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:55:15 - Current CPU temperature: 51C
2024-01-02 17:56:16 - DIMM0 temperature: 0.000C
2024-01-02 17:56:16 - DIMM1 temperature: 0.000C
2024-01-02 17:56:16 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:56:16 - Current CPU temperature: 50C
2024-01-02 17:57:16 - DIMM0 temperature: 0.000C
2024-01-02 17:57:16 - DIMM1 temperature: 0.000C
2024-01-02 17:57:16 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:57:16 - Current CPU temperature: 50C
2024-01-02 17:58:17 - DIMM0 temperature: 0.000C
2024-01-02 17:58:17 - DIMM1 temperature: 0.000C
2024-01-02 17:58:17 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:58:17 - Current CPU temperature: 50C
2024-01-02 17:59:18 - DIMM0 temperature: 0.000C
2024-01-02 17:59:18 - DIMM1 temperature: 0.000C
2024-01-02 17:59:18 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 17:59:18 - Current CPU temperature: 50C
2024-01-02 18:00:18 - DIMM0 temperature: 0.000C
2024-01-02 18:00:18 - DIMM1 temperature: 0.000C
2024-01-02 18:00:18 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:00:18 - Current CPU temperature: 50C
2024-01-02 18:01:19 - DIMM0 temperature: 0.000C
2024-01-02 18:01:19 - DIMM1 temperature: 0.000C
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2024-01-02 18:01:19 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:01:19 - Current CPU temperature: 50C
2024-01-02 18:02:19 - DIMM0 temperature: 0.000C
2024-01-02 18:02:19 - DIMM1 temperature: 0.000C
2024-01-02 18:02:20 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:02:20 - Current CPU temperature: 50C
2024-01-02 18:02:39 - MtSupportRunAllTests - Test execution time: 505.224s (Test 9 cumulative
error count: 0, buffer full count: 0)
2024-01-02 18:02:39 - Running test #10 (Test 10 [Bit fade test, 2 patterns, 1 CPU])
2024-01-02 18:02:39 - MtSupportRunAllTests - Setting random seed to 0xA6A0FDC0
2024-01-02 18:02:39 - MtSupportRunAllTests - Start time: 3335677 ms
2024-01-02 18:02:39 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:02:39 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:02:39 - Start bit fade test (0x0 - 0x41F38000)
2024-01-02 18:02:39 - Starting Stage 0, Pattern=00000000
2024-01-02 18:02:42 - Finished Stage 0, Pattern=00000000
2024-01-02 18:02:42 - Sleep start time: 3338683
2024-01-02 18:02:42 - Sleep time: 300000
2024-01-02 18:02:42 - Test 10 - Sleeping (299 seconds remaining)
2024-01-02 18:03:12 - Slept for 30 seconds
2024-01-02 18:03:20 - DIMM0 temperature: 0.000C
2024-01-02 18:03:20 - DIMM1 temperature: 0.000C
2024-01-02 18:03:20 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:03:20 - Current CPU temperature: 34C
2024-01-02 18:03:43 - Slept for 60 seconds
2024-01-02 18:04:13 - Slept for 90 seconds
2024-01-02 18:04:21 - DIMM0 temperature: 0.000C
2024-01-02 18:04:21 - DIMM1 temperature: 0.000C
2024-01-02 18:04:21 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:04:21 - Current CPU temperature: 32C
2024-01-02 18:04:43 - Slept for 120 seconds
2024-01-02 18:05:13 - Slept for 150 seconds
2024-01-02 18:05:21 - DIMM0 temperature: 0.000C
2024-01-02 18:05:21 - DIMM1 temperature: 0.000C
2024-01-02 18:05:21 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:05:21 - Current CPU temperature: 30C
2024-01-02 18:05:44 - Slept for 180 seconds
2024-01-02 18:06:14 - Slept for 210 seconds
2024-01-02 18:06:22 - DIMM0 temperature: 0.000C
2024-01-02 18:06:22 - DIMM1 temperature: 0.000C
2024-01-02 18:06:22 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:06:22 - Current CPU temperature: 30C
2024-01-02 18:06:44 - Slept for 240 seconds
2024-01-02 18:07:14 - Slept for 270 seconds
2024-01-02 18:07:23 - DIMM0 temperature: 0.000C
2024-01-02 18:07:23 - DIMM1 temperature: 0.000C
2024-01-02 18:07:23 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:07:23 - Current CPU temperature: 30C
2024-01-02 18:07:44 - Starting Stage 1, Pattern=00000000
2024-01-02 18:07:47 - Finished Stage 1, Pattern=00000000
2024-01-02 18:07:47 - Starting Stage 0, Pattern=FFFFFFF
2024-01-02 18:07:50 - Finished Stage 0, Pattern=FFFFFFF
2024-01-02 18:07:50 - Sleep start time: 3644888
2024-01-02 18:07:50 - Sleep time: 300000
2024-01-02 18:07:50 - Test 10 - Sleeping (299 seconds remaining)
2024-01-02 18:08:20 - Slept for 30 seconds
2024-01-02 18:08:23 - DIMM0 temperature: 0.000C
2024-01-02 18:08:23 - DIMM1 temperature: 0.000C
2024-01-02 18:08:23 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:08:24 - Current CPU temperature: 30C
2024-01-02 18:08:51 - Slept for 60 seconds
2024-01-02 18:09:21 - Slept for 90 seconds
2024-01-02 18:09:24 - DIMM0 temperature: 0.000C
2024-01-02 18:09:24 - DIMM1 temperature: 0.000C
2024-01-02 18:09:24 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:09:24 - Current CPU temperature: 30C
2024-01-02 18:09:51 - Slept for 120 seconds
2024-01-02 18:10:21 - Slept for 150 seconds
2024-01-02 18:10:25 - DIMM0 temperature: 0.000C
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2024-01-02 18:10:25 - DIMM1 temperature: 0.000C
2024-01-02 18:10:25 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:10:25 - Current CPU temperature: 30C
2024-01-02 18:10:52 - Slept for 180 seconds
2024-01-02 18:11:22 - Slept for 210 seconds
2024-01-02 18:11:25 - DIMM0 temperature: 0.000C
2024-01-02 18:11:25 - DIMM1 temperature: 0.000C
2024-01-02 18:11:25 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:11:25 - Current CPU temperature: 30C
2024-01-02 18:11:52 - Slept for 240 seconds
2024-01-02 18:12:22 - Slept for 270 seconds
2024-01-02 18:12:26 - DIMM0 temperature: 0.000C
2024-01-02 18:12:26 - DIMM1 temperature: 0.000C
2024-01-02 18:12:26 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:12:26 - Current CPU temperature: 30C
2024-01-02 18:12:52 - Starting Stage 1, Pattern=FFFFFFFF
2024-01-02 18:12:55 - Finished Stage 1, Pattern=FFFFFFFF
2024-01-02 18:12:55 - MtSupportRunAllTests - Test execution time: 612.510s (Test 10 cumulative
error count: 0, buffer full count: 0)
2024-01-02 18:12:56 - Running test #13 (Test 13 [Hammer test])
2024-01-02 18:12:56 - MtSupportRunAllTests - Setting random seed to 0x847D2644
2024-01-02 18:12:56 - MtSupportRunAllTests - Start time: 3948483 ms
2024-01-02 18:12:56 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:12:56 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:12:56 - Start double-sided hammer test (0x0 - 0x41F38000, Step size: 0x1000000)
2024-01-02 18:12:56 - Using random pattern
2024-01-02 18:12:56 - # of segments to hammer: 7 (SegSize=2048MB, SkipSize=0MB)
2024-01-02 18:13:26 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0xB020000 -
0x8B020000]
2024-01-02 18:13:27 - DIMM0 temperature: 0.000C
2024-01-02 18:13:27 - DIMM1 temperature: 0.000C
2024-01-02 18:13:27 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:13:27 - Current CPU temperature: 40C
2024-01-02 18:13:51 - RunHammerTest - Running hammer test on 2 CPUs (589MB each) [0x8B020000 -
0xD4A39000]
2024-01-02 18:14:27 - DIMM0 temperature: 0.000C
2024-01-02 18:14:27 - DIMM1 temperature: 0.000C
2024-01-02 18:14:27 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:14:27 - Current CPU temperature: 41C
2024-01-02 18:14:35 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x100000000 -
0x180000000]
2024-01-02 18:15:00 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x180000000 -
0x200000000]
2024-01-02 18:15:25 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x200000000 -
0x280000000]
2024-01-02 18:15:28 - DIMM0 temperature: 0.000C
2024-01-02 18:15:28 - DIMM1 temperature: 0.000C
2024-01-02 18:15:28 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:15:28 - Current CPU temperature: 38C
2024-01-02 18:15:50 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x280000000 -
0x300000000]
2024-01-02 18:16:15 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x300000000 -
0x380000000]
2024-01-02 18:16:28 - DIMM0 temperature: 0.000C
2024-01-02 18:16:28 - DIMM1 temperature: 0.000C
2024-01-02 18:16:29 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:16:29 - Current CPU temperature: 42C
2024-01-02 18:16:41 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x380000000 -
0x400000000]
2024-01-02 18:17:19 - MtSupportRunAllTests - Test execution time: 262.066s (Test 13 cumulative
error count: 0, buffer full count: 0)
2024-01-02 18:17:20 - Finished pass #2 (of 4) (Cumulative error count: 0, buffer full count: 0)
2024-01-02 18:17:20 - Starting pass #3 (of 4)
2024-01-02 18:17:20 - Running test #0 (Test 0 [Address test, walking ones, 1 CPU])
2024-01-02 18:17:20 - MtSupportRunAllTests - Setting random seed to 0x6639FD48
2024-01-02 18:17:20 - MtSupportRunAllTests - Start time: 4210872 ms
2024-01-02 18:17:20 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:17:20 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:17:20 - Start memory range test (0x0 - 0x41F38000)
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2024-01-02 18:17:20 - MtSupportRunAllTests - Test execution time: 0.422s (Test 0 cumulative error count: 0, buffer full count: 0)
2024-01-02 18:17:20 - Running test #1 (Test 1 [Address test, own address, 1 CPU])
2024-01-02 18:17:20 - MtSupportRunAllTests - Setting random seed to 0xF53F6494
2024-01-02 18:17:20 - MtSupportRunAllTests - Start time: 4211536 ms
2024-01-02 18:17:20 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:17:21 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:17:21 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:17:25 - MtSupportRunAllTests - Test execution time: 4.383s (Test 1 cumulative error count: 0, buffer full count: 0)
2024-01-02 18:17:25 - Running test #2 (Test 2 [Address test, own address])
2024-01-02 18:17:25 - MtSupportRunAllTests - Setting random seed to 0xE9190144
2024-01-02 18:17:25 - MtSupportRunAllTests - Start time: 4216231 ms
2024-01-02 18:17:25 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:17:25 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:17:25 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:17:27 - MtSupportRunAllTests - Test execution time: 2.017s (Test 2 cumulative error count: 0, buffer full count: 0)
2024-01-02 18:17:27 - Running test #3 (Test 3 [Moving inversions, ones & zeroes])
2024-01-02 18:17:27 - MtSupportRunAllTests - Setting random seed to 0xCFB4BC08
2024-01-02 18:17:27 - MtSupportRunAllTests - Start time: 4218490 ms
2024-01-02 18:17:27 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:17:27 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:17:27 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:17:29 - DIMM0 temperature: 0.000C
2024-01-02 18:17:29 - DIMM1 temperature: 0.000C
2024-01-02 18:17:29 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:17:29 - Current CPU temperature: 44C
2024-01-02 18:18:04 - MtSupportRunAllTests - Test execution time: 36.759s (Test 3 cumulative error count: 0, buffer full count: 0)
2024-01-02 18:18:05 - Running test #4 (Test 4 [Moving inversions, 8-bit pattern])
2024-01-02 18:18:05 - MtSupportRunAllTests - Setting random seed to 0xF6180AC8
2024-01-02 18:18:05 - MtSupportRunAllTests - Start time: 4255493 ms
2024-01-02 18:18:05 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:18:05 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:18:05 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:18:30 - DIMM0 temperature: 0.000C
2024-01-02 18:18:30 - DIMM1 temperature: 0.000C
2024-01-02 18:18:30 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:18:30 - Current CPU temperature: 48C
2024-01-02 18:19:30 - DIMM0 temperature: 0.000C
2024-01-02 18:19:31 - DIMM1 temperature: 0.000C
2024-01-02 18:19:31 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:19:31 - Current CPU temperature: 49C
2024-01-02 18:20:31 - DIMM0 temperature: 0.000C
2024-01-02 18:20:31 - DIMM1 temperature: 0.000C
2024-01-02 18:20:31 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:20:31 - Current CPU temperature: 49C
2024-01-02 18:20:44 - MtSupportRunAllTests - Test execution time: 158.616s (Test 4 cumulative error count: 0, buffer full count: 0)
2024-01-02 18:20:44 - Running test #5 (Test 5 [Moving inversions, random pattern])
2024-01-02 18:20:44 - MtSupportRunAllTests - Setting random seed to 0xB547D48C
2024-01-02 18:20:44 - MtSupportRunAllTests - Start time: 4414372 ms
2024-01-02 18:20:45 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:20:45 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:20:45 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:21:32 - DIMM0 temperature: 0.000C
2024-01-02 18:21:32 - DIMM1 temperature: 0.000C
2024-01-02 18:21:32 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:21:32 - Current CPU temperature: 50C
2024-01-02 18:22:32 - DIMM0 temperature: 0.000C
2024-01-02 18:22:32 - DIMM1 temperature: 0.000C
2024-01-02 18:22:32 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:22:33 - Current CPU temperature: 50C
2024-01-02 18:23:33 - DIMM0 temperature: 0.000C
2024-01-02 18:23:33 - DIMM1 temperature: 0.000C
2024-01-02 18:23:33 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:23:33 - Current CPU temperature: 50C
2024-01-02 18:24:19 - MtSupportRunAllTests - Test execution time: 212.638s (Test 5 cumulative


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error count: 0, buffer full count: 0)
2024-01-02 18:24:19 - Running test #6 (Test 6 [Block move, 64-byte blocks])
2024-01-02 18:24:19 - MtSupportRunAllTests - Setting random seed to 0xF5307FF8
2024-01-02 18:24:19 - MtSupportRunAllTests - Start time: 4627306 ms
2024-01-02 18:24:19 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:24:19 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:24:19 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:24:34 - DIMM0 temperature: 0.000C
2024-01-02 18:24:34 - DIMM1 temperature: 0.000C
2024-01-02 18:24:34 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:24:34 - Current CPU temperature: 45C
2024-01-02 18:25:34 - DIMM0 temperature: 0.000C
2024-01-02 18:25:35 - DIMM1 temperature: 0.000C
2024-01-02 18:25:35 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:25:35 - Current CPU temperature: 44C
2024-01-02 18:26:35 - DIMM0 temperature: 0.000C
2024-01-02 18:26:35 - DIMM1 temperature: 0.000C
2024-01-02 18:26:35 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:26:35 - Current CPU temperature: 44C
2024-01-02 18:26:47 - MtSupportRunAllTests - Test execution time: 147.407s (Test 6 cumulative
error count: 0, buffer full count: 0)
2024-01-02 18:26:47 - Running test #7 (Test 7 [Moving inversions, 32-bit pattern])
2024-01-02 18:26:47 - MtSupportRunAllTests - Setting random seed to 0x41EFB600
2024-01-02 18:26:47 - MtSupportRunAllTests - Start time: 4774964 ms
2024-01-02 18:26:47 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:26:47 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:26:47 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:27:36 - DIMM0 temperature: 0.000C
2024-01-02 18:27:36 - DIMM1 temperature: 0.000C
2024-01-02 18:27:36 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:27:36 - Current CPU temperature: 53C
2024-01-02 18:28:37 - DIMM0 temperature: 0.000C
2024-01-02 18:28:37 - DIMM1 temperature: 0.000C
2024-01-02 18:28:37 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:28:37 - Current CPU temperature: 54C
2024-01-02 18:29:38 - DIMM0 temperature: 0.000C
2024-01-02 18:29:38 - DIMM1 temperature: 0.000C
2024-01-02 18:29:38 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:29:38 - Current CPU temperature: 54C
2024-01-02 18:30:38 - DIMM0 temperature: 0.000C
2024-01-02 18:30:38 - DIMM1 temperature: 0.000C
2024-01-02 18:30:39 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:30:39 - Current CPU temperature: 54C
2024-01-02 18:31:39 - DIMM0 temperature: 0.000C
2024-01-02 18:31:39 - DIMM1 temperature: 0.000C
2024-01-02 18:31:39 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:31:39 - Current CPU temperature: 54C
2024-01-02 18:32:40 - DIMM0 temperature: 0.000C
2024-01-02 18:32:40 - DIMM1 temperature: 0.000C
2024-01-02 18:32:40 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:32:40 - Current CPU temperature: 54C
2024-01-02 18:33:41 - DIMM0 temperature: 0.000C
2024-01-02 18:33:41 - DIMM1 temperature: 0.000C
2024-01-02 18:33:41 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:33:41 - Current CPU temperature: 55C
2024-01-02 18:34:42 - DIMM0 temperature: 0.000C
2024-01-02 18:34:42 - DIMM1 temperature: 0.000C
2024-01-02 18:34:42 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:34:42 - Current CPU temperature: 55C
2024-01-02 18:35:42 - DIMM0 temperature: 0.000C
2024-01-02 18:35:42 - DIMM1 temperature: 0.000C
2024-01-02 18:35:43 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:35:43 - Current CPU temperature: 54C
2024-01-02 18:36:43 - DIMM0 temperature: 0.000C
2024-01-02 18:36:43 - DIMM1 temperature: 0.000C
2024-01-02 18:36:43 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:36:43 - Current CPU temperature: 54C
2024-01-02 18:37:28 - MtSupportRunAllTests - Test execution time: 636.143s (Test 7 cumulative
error count: 0, buffer full count: 0)
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2024-01-02 18:37:28 - Running test #8 (Test 8 [Random number sequence])
2024-01-02 18:37:28 - MtSupportRunAllTests - Setting random seed to 0xFB4643D0
2024-01-02 18:37:28 - MtSupportRunAllTests - Start time: 5411360 ms
2024-01-02 18:37:28 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:37:28 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:37:28 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:37:44 - DIMM0 temperature: 0.000C
2024-01-02 18:37:44 - DIMM1 temperature: 0.000C
2024-01-02 18:37:44 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:37:44 - Current CPU temperature: 64C
2024-01-02 18:38:45 - DIMM0 temperature: 0.000C
2024-01-02 18:38:45 - DIMM1 temperature: 0.000C
2024-01-02 18:38:45 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:38:45 - Current CPU temperature: 65C
2024-01-02 18:39:12 - MtSupportRunAllTests - Test execution time: 103.969s (Test 8 cumulative
error count: 0, buffer full count: 0)
2024-01-02 18:39:12 - Running test #9 (Test 9 [Modulo 20, ones & zeros])
2024-01-02 18:39:12 - MtSupportRunAllTests - Setting random seed to 0xB5E13E28
2024-01-02 18:39:13 - MtSupportRunAllTests - Start time: 5515574 ms
2024-01-02 18:39:13 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:39:13 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:39:13 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 18:39:45 - DIMM0 temperature: 0.000C
2024-01-02 18:39:45 - DIMM1 temperature: 0.000C
2024-01-02 18:39:46 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:39:46 - Current CPU temperature: 53C
2024-01-02 18:40:46 - DIMM0 temperature: 0.000C
2024-01-02 18:40:46 - DIMM1 temperature: 0.000C
2024-01-02 18:40:46 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:40:46 - Current CPU temperature: 52C
2024-01-02 18:41:47 - DIMM0 temperature: 0.000C
2024-01-02 18:41:47 - DIMM1 temperature: 0.000C
2024-01-02 18:41:47 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:41:47 - Current CPU temperature: 51C
2024-01-02 18:42:47 - DIMM0 temperature: 0.000C
2024-01-02 18:42:47 - DIMM1 temperature: 0.000C
2024-01-02 18:42:47 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:42:47 - Current CPU temperature: 51C
2024-01-02 18:43:48 - DIMM0 temperature: 0.000C
2024-01-02 18:43:48 - DIMM1 temperature: 0.000C
2024-01-02 18:43:48 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:43:48 - Current CPU temperature: 50C
2024-01-02 18:44:49 - DIMM0 temperature: 0.000C
2024-01-02 18:44:49 - DIMM1 temperature: 0.000C
2024-01-02 18:44:49 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:44:49 - Current CPU temperature: 50C
2024-01-02 18:45:49 - DIMM0 temperature: 0.000C
2024-01-02 18:45:49 - DIMM1 temperature: 0.000C
2024-01-02 18:45:49 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:45:49 - Current CPU temperature: 51C
2024-01-02 18:46:50 - DIMM0 temperature: 0.000C
2024-01-02 18:46:50 - DIMM1 temperature: 0.000C
2024-01-02 18:46:50 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:46:50 - Current CPU temperature: 50C
2024-01-02 18:47:41 - MtSupportRunAllTests - Test execution time: 505.329s (Test 9 cumulative
error count: 0, buffer full count: 0)
2024-01-02 18:47:41 - Running test #10 (Test 10 [Bit fade test, 2 patterns, 1 CPU])
2024-01-02 18:47:41 - MtSupportRunAllTests - Setting random seed to 0x596AD288
2024-01-02 18:47:41 - MtSupportRunAllTests - Start time: 6021198 ms
2024-01-02 18:47:41 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:47:41 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:47:41 - Start bit fade test (0x0 - 0x41F38000)
2024-01-02 18:47:41 - Starting Stage 0, Pattern=00000000
2024-01-02 18:47:44 - Finished Stage 0, Pattern=00000000
2024-01-02 18:47:44 - Sleep start time: 6024209
2024-01-02 18:47:44 - Sleep time: 300000
2024-01-02 18:47:44 - Test 10 - Sleeping (299 seconds remaining)
2024-01-02 18:47:50 - DIMM0 temperature: 0.000C
2024-01-02 18:47:50 - DIMM1 temperature: 0.000C
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2024-01-02 18:47:50 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:47:50 - Current CPU temperature: 37C
2024-01-02 18:48:15 - Slept for 30 seconds
2024-01-02 18:48:45 - Slept for 60 seconds
2024-01-02 18:48:51 - DIMM0 temperature: 0.000C
2024-01-02 18:48:51 - DIMM1 temperature: 0.000C
2024-01-02 18:48:51 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:48:51 - Current CPU temperature: 33C
2024-01-02 18:49:15 - Slept for 90 seconds
2024-01-02 18:49:45 - Slept for 120 seconds
2024-01-02 18:49:52 - DIMM0 temperature: 0.000C
2024-01-02 18:49:52 - DIMM1 temperature: 0.000C
2024-01-02 18:49:52 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:49:52 - Current CPU temperature: 32C
2024-01-02 18:50:16 - Slept for 150 seconds
2024-01-02 18:50:46 - Slept for 180 seconds
2024-01-02 18:50:52 - DIMM0 temperature: 0.000C
2024-01-02 18:50:52 - DIMM1 temperature: 0.000C
2024-01-02 18:50:52 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:50:52 - Current CPU temperature: 31C
2024-01-02 18:51:16 - Slept for 210 seconds
2024-01-02 18:51:46 - Slept for 240 seconds
2024-01-02 18:51:53 - DIMM0 temperature: 0.000C
2024-01-02 18:51:53 - DIMM1 temperature: 0.000C
2024-01-02 18:51:53 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:51:53 - Current CPU temperature: 31C
2024-01-02 18:52:17 - Slept for 270 seconds
2024-01-02 18:52:46 - Starting Stage 1, Pattern=00000000
2024-01-02 18:52:50 - Finished Stage 1, Pattern=00000000
2024-01-02 18:52:50 - Starting Stage 0, Pattern=FFFFFFFF
2024-01-02 18:52:52 - Finished Stage 0, Pattern=FFFFFFFF
2024-01-02 18:52:52 - Sleep start time: 6330457
2024-01-02 18:52:53 - Sleep time: 300000
2024-01-02 18:52:53 - Test 10 - Sleeping (299 seconds remaining)
2024-01-02 18:52:54 - DIMM0 temperature: 0.000C
2024-01-02 18:52:54 - DIMM1 temperature: 0.000C
2024-01-02 18:52:54 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:52:54 - Current CPU temperature: 37C
2024-01-02 18:53:23 - Slept for 30 seconds
2024-01-02 18:53:53 - Slept for 60 seconds
2024-01-02 18:53:54 - DIMM0 temperature: 0.000C
2024-01-02 18:53:54 - DIMM1 temperature: 0.000C
2024-01-02 18:53:54 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:53:54 - Current CPU temperature: 31C
2024-01-02 18:54:23 - Slept for 90 seconds
2024-01-02 18:54:54 - Slept for 120 seconds
2024-01-02 18:54:55 - DIMM0 temperature: 0.000C
2024-01-02 18:54:55 - DIMM1 temperature: 0.000C
2024-01-02 18:54:55 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:54:55 - Current CPU temperature: 30C
2024-01-02 18:55:24 - Slept for 150 seconds
2024-01-02 18:55:54 - Slept for 180 seconds
2024-01-02 18:55:56 - DIMM0 temperature: 0.000C
2024-01-02 18:55:56 - DIMM1 temperature: 0.000C
2024-01-02 18:55:56 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:55:56 - Current CPU temperature: 30C
2024-01-02 18:56:24 - Slept for 210 seconds
2024-01-02 18:56:54 - Slept for 240 seconds
2024-01-02 18:56:56 - DIMM0 temperature: 0.000C
2024-01-02 18:56:56 - DIMM1 temperature: 0.000C
2024-01-02 18:56:56 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:56:56 - Current CPU temperature: 30C
2024-01-02 18:57:25 - Slept for 270 seconds
2024-01-02 18:57:54 - Starting Stage 1, Pattern=FFFFFFFF
2024-01-02 18:57:57 - DIMM0 temperature: 0.000C
2024-01-02 18:57:57 - DIMM1 temperature: 0.000C
2024-01-02 18:57:57 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:57:57 - Current CPU temperature: 40C
2024-01-02 18:57:58 - Finished Stage 1, Pattern=FFFFFFFF

2024-01-02 18:57:58 - MtSupportRunAllTests - Test execution time: 612.721s (Test 10 cumulative error count: 0, buffer full count: 0)
2024-01-02 18:57:58 - Running test #13 (Test 13 [Hammer test])
2024-01-02 18:57:58 - MtSupportRunAllTests - Setting random seed to 0x59794438
2024-01-02 18:57:58 - MtSupportRunAllTests - Start time: 6634163 ms
2024-01-02 18:57:58 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 18:57:58 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 18:57:58 - Start double-sided hammer test (0x0 - 0x41F38000, Step size: 0x1000000)
2024-01-02 18:57:58 - Using random pattern
2024-01-02 18:57:58 - # of segments to hammer: 7 (SegSize=2048MB, SkipSize=0MB)
2024-01-02 18:58:28 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0xB020000 - 0x8B020000]
2024-01-02 18:58:54 - RunHammerTest - Running hammer test on 2 CPUs (589MB each) [0x8B020000 - 0xD4A39000]
2024-01-02 18:58:58 - DIMM0 temperature: 0.000C
2024-01-02 18:58:58 - DIMM1 temperature: 0.000C
2024-01-02 18:58:58 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:58:58 - Current CPU temperature: 36C
2024-01-02 18:59:40 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x100000000 - 0x180000000]
2024-01-02 18:59:58 - DIMM0 temperature: 0.000C
2024-01-02 18:59:58 - DIMM1 temperature: 0.000C
2024-01-02 18:59:58 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 18:59:58 - Current CPU temperature: 43C
2024-01-02 19:00:05 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x180000000 - 0x200000000]
2024-01-02 19:00:30 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x200000000 - 0x280000000]
2024-01-02 19:00:56 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x280000000 - 0x300000000]
2024-01-02 19:00:59 - DIMM0 temperature: 0.000C
2024-01-02 19:00:59 - DIMM1 temperature: 0.000C
2024-01-02 19:00:59 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:00:59 - Current CPU temperature: 38C
2024-01-02 19:01:21 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x300000000 - 0x380000000]
2024-01-02 19:01:46 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x380000000 - 0x400000000]
2024-01-02 19:01:59 - DIMM0 temperature: 0.000C
2024-01-02 19:01:59 - DIMM1 temperature: 0.000C
2024-01-02 19:02:00 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:02:00 - Current CPU temperature: 44C
2024-01-02 19:02:25 - MtSupportRunAllTests - Test execution time: 265.641s (Test 13 cumulative error count: 0, buffer full count: 0)
2024-01-02 19:02:26 - Finished pass #3 (of 4) (Cumulative error count: 0, buffer full count: 0)
2024-01-02 19:02:26 - Starting pass #4 (of 4)
2024-01-02 19:02:26 - Running test #0 (Test 0 [Address test, walking ones, 1 CPU])
2024-01-02 19:02:26 - MtSupportRunAllTests - Setting random seed to 0x3D606F1C
2024-01-02 19:02:26 - MtSupportRunAllTests - Start time: 6900167 ms
2024-01-02 19:02:26 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:02:26 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:02:26 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:02:26 - MtSupportRunAllTests - Test execution time: 0.423s (Test 0 cumulative error count: 0, buffer full count: 0)
2024-01-02 19:02:26 - Running test #1 (Test 1 [Address test, own address, 1 CPU])
2024-01-02 19:02:26 - MtSupportRunAllTests - Setting random seed to 0xD6255448
2024-01-02 19:02:26 - MtSupportRunAllTests - Start time: 6900835 ms
2024-01-02 19:02:26 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:02:26 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:02:27 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:02:31 - MtSupportRunAllTests - Test execution time: 4.384s (Test 1 cumulative error count: 0, buffer full count: 0)
2024-01-02 19:02:31 - Running test #2 (Test 2 [Address test, own address])
2024-01-02 19:02:31 - MtSupportRunAllTests - Setting random seed to 0xBDA3BB80
2024-01-02 19:02:31 - MtSupportRunAllTests - Start time: 6905524 ms
2024-01-02 19:02:31 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:02:31 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:02:31 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:02:33 - MtSupportRunAllTests - Test execution time: 2.020s (Test 2 cumulative error

```
count: 0, buffer full count: 0)
2024-01-02 19:02:33 - Running test #3 (Test 3 [Moving inversions, ones & zeroes])
2024-01-02 19:02:33 - MtSupportRunAllTests - Setting random seed to 0xB0E03118
2024-01-02 19:02:33 - MtSupportRunAllTests - Start time: 6907790 ms
2024-01-02 19:02:33 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:02:33 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:02:34 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:03:00 - DIMM0 temperature: 0.000C
2024-01-02 19:03:00 - DIMM1 temperature: 0.000C
2024-01-02 19:03:00 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:03:00 - Current CPU temperature: 47C
2024-01-02 19:03:11 - MtSupportRunAllTests - Test execution time: 36.769s (Test 3 cumulative error
count: 0, buffer full count: 0)
2024-01-02 19:03:11 - Running test #4 (Test 4 [Moving inversions, 8-bit pattern])
2024-01-02 19:03:11 - MtSupportRunAllTests - Setting random seed to 0xD99F1E9C
2024-01-02 19:03:11 - MtSupportRunAllTests - Start time: 6944804 ms
2024-01-02 19:03:11 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:03:11 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:03:11 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:04:01 - DIMM0 temperature: 0.000C
2024-01-02 19:04:01 - DIMM1 temperature: 0.000C
2024-01-02 19:04:01 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:04:01 - Current CPU temperature: 49C
2024-01-02 19:05:01 - DIMM0 temperature: 0.000C
2024-01-02 19:05:01 - DIMM1 temperature: 0.000C
2024-01-02 19:05:01 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:05:02 - Current CPU temperature: 49C
2024-01-02 19:05:50 - MtSupportRunAllTests - Test execution time: 158.644s (Test 4 cumulative
error count: 0, buffer full count: 0)
2024-01-02 19:05:50 - Running test #5 (Test 5 [Moving inversions, random pattern])
2024-01-02 19:05:51 - MtSupportRunAllTests - Setting random seed to 0xA55173DC
2024-01-02 19:05:51 - MtSupportRunAllTests - Start time: 7103743 ms
2024-01-02 19:05:51 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:05:51 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:05:51 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:06:02 - DIMM0 temperature: 0.000C
2024-01-02 19:06:02 - DIMM1 temperature: 0.000C
2024-01-02 19:06:02 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:06:02 - Current CPU temperature: 49C
2024-01-02 19:07:03 - DIMM0 temperature: 0.000C
2024-01-02 19:07:03 - DIMM1 temperature: 0.000C
2024-01-02 19:07:03 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:07:03 - Current CPU temperature: 50C
2024-01-02 19:08:03 - DIMM0 temperature: 0.000C
2024-01-02 19:08:03 - DIMM1 temperature: 0.000C
2024-01-02 19:08:03 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:08:03 - Current CPU temperature: 50C
2024-01-02 19:09:04 - DIMM0 temperature: 0.000C
2024-01-02 19:09:04 - DIMM1 temperature: 0.000C
2024-01-02 19:09:04 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:09:04 - Current CPU temperature: 50C
2024-01-02 19:09:25 - MtSupportRunAllTests - Test execution time: 212.652s (Test 5 cumulative
error count: 0, buffer full count: 0)
2024-01-02 19:09:25 - Running test #6 (Test 6 [Block move, 64-byte blocks])
2024-01-02 19:09:25 - MtSupportRunAllTests - Setting random seed to 0xE0EC0CF8
2024-01-02 19:09:25 - MtSupportRunAllTests - Start time: 7316663 ms
2024-01-02 19:09:25 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:09:25 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:09:25 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:10:05 - DIMM0 temperature: 0.000C
2024-01-02 19:10:05 - DIMM1 temperature: 0.000C
2024-01-02 19:10:05 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:10:05 - Current CPU temperature: 45C
2024-01-02 19:11:05 - DIMM0 temperature: 0.000C
2024-01-02 19:11:05 - DIMM1 temperature: 0.000C
2024-01-02 19:11:05 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:11:06 - Current CPU temperature: 45C
2024-01-02 19:11:53 - MtSupportRunAllTests - Test execution time: 147.391s (Test 6 cumulative
error count: 0, buffer full count: 0)
```

```
2024-01-02 19:11:53 - Running test #7 (Test 7 [Moving inversions, 32-bit pattern])
2024-01-02 19:11:53 - MtSupportRunAllTests - Setting random seed to 0x29D59A88
2024-01-02 19:11:53 - MtSupportRunAllTests - Start time: 7464296 ms
2024-01-02 19:11:53 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:11:53 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:11:53 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:12:06 - DIMM0 temperature: 0.000C
2024-01-02 19:12:06 - DIMM1 temperature: 0.000C
2024-01-02 19:12:07 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:12:07 - Current CPU temperature: 51C
2024-01-02 19:13:07 - DIMM0 temperature: 0.000C
2024-01-02 19:13:07 - DIMM1 temperature: 0.000C
2024-01-02 19:13:07 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:13:07 - Current CPU temperature: 53C
2024-01-02 19:14:08 - DIMM0 temperature: 0.000C
2024-01-02 19:14:08 - DIMM1 temperature: 0.000C
2024-01-02 19:14:08 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:14:08 - Current CPU temperature: 55C
2024-01-02 19:15:09 - DIMM0 temperature: 0.000C
2024-01-02 19:15:09 - DIMM1 temperature: 0.000C
2024-01-02 19:15:09 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:15:09 - Current CPU temperature: 55C
2024-01-02 19:16:09 - DIMM0 temperature: 0.000C
2024-01-02 19:16:09 - DIMM1 temperature: 0.000C
2024-01-02 19:16:10 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:16:10 - Current CPU temperature: 55C
2024-01-02 19:17:10 - DIMM0 temperature: 0.000C
2024-01-02 19:17:10 - DIMM1 temperature: 0.000C
2024-01-02 19:17:10 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:17:10 - Current CPU temperature: 55C
2024-01-02 19:18:11 - DIMM0 temperature: 0.000C
2024-01-02 19:18:11 - DIMM1 temperature: 0.000C
2024-01-02 19:18:11 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:18:11 - Current CPU temperature: 55C
2024-01-02 19:19:12 - DIMM0 temperature: 0.000C
2024-01-02 19:19:12 - DIMM1 temperature: 0.000C
2024-01-02 19:19:12 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:19:12 - Current CPU temperature: 55C
2024-01-02 19:20:13 - DIMM0 temperature: 0.000C
2024-01-02 19:20:13 - DIMM1 temperature: 0.000C
2024-01-02 19:20:13 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:20:13 - Current CPU temperature: 55C
2024-01-02 19:21:13 - DIMM0 temperature: 0.000C
2024-01-02 19:21:13 - DIMM1 temperature: 0.000C
2024-01-02 19:21:13 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:21:13 - Current CPU temperature: 55C
2024-01-02 19:22:14 - DIMM0 temperature: 0.000C
2024-01-02 19:22:14 - DIMM1 temperature: 0.000C
2024-01-02 19:22:14 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:22:14 - Current CPU temperature: 55C
2024-01-02 19:22:33 - MtSupportRunAllTests - Test execution time: 635.780s (Test 7 cumulative
error count: 0, buffer full count: 0)
2024-01-02 19:22:33 - Running test #8 (Test 8 [Random number sequence])
2024-01-02 19:22:33 - MtSupportRunAllTests - Setting random seed to 0x9BAAF290
2024-01-02 19:22:33 - MtSupportRunAllTests - Start time: 8100360 ms
2024-01-02 19:22:33 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:22:33 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:22:33 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:23:14 - DIMM0 temperature: 0.000C
2024-01-02 19:23:15 - DIMM1 temperature: 0.000C
2024-01-02 19:23:15 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:23:15 - Current CPU temperature: 65C
2024-01-02 19:24:15 - DIMM0 temperature: 0.000C
2024-01-02 19:24:15 - DIMM1 temperature: 0.000C
2024-01-02 19:24:15 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:24:15 - Current CPU temperature: 66C
2024-01-02 19:24:19 - MtSupportRunAllTests - Test execution time: 104.481s (Test 8 cumulative
error count: 0, buffer full count: 0)
2024-01-02 19:24:19 - Running test #9 (Test 9 [Modulo 20, ones & zeros])
```

```
2024-01-02 19:24:19 - MtSupportRunAllTests - Setting random seed to 0xCF4602B0
2024-01-02 19:24:19 - MtSupportRunAllTests - Start time: 8205136 ms
2024-01-02 19:24:19 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:24:19 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:24:19 - Start memory range test (0x0 - 0x41F38000)
2024-01-02 19:25:16 - DIMM0 temperature: 0.000C
2024-01-02 19:25:16 - DIMM1 temperature: 0.000C
2024-01-02 19:25:16 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:25:16 - Current CPU temperature: 53C
2024-01-02 19:26:16 - DIMM0 temperature: 0.000C
2024-01-02 19:26:16 - DIMM1 temperature: 0.000C
2024-01-02 19:26:16 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:26:16 - Current CPU temperature: 52C
2024-01-02 19:27:17 - DIMM0 temperature: 0.000C
2024-01-02 19:27:17 - DIMM1 temperature: 0.000C
2024-01-02 19:27:17 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:27:17 - Current CPU temperature: 52C
2024-01-02 19:28:18 - DIMM0 temperature: 0.000C
2024-01-02 19:28:18 - DIMM1 temperature: 0.000C
2024-01-02 19:28:18 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:28:18 - Current CPU temperature: 52C
2024-01-02 19:29:18 - DIMM0 temperature: 0.000C
2024-01-02 19:29:18 - DIMM1 temperature: 0.000C
2024-01-02 19:29:18 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:29:18 - Current CPU temperature: 51C
2024-01-02 19:30:19 - DIMM0 temperature: 0.000C
2024-01-02 19:30:19 - DIMM1 temperature: 0.000C
2024-01-02 19:30:19 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:30:19 - Current CPU temperature: 52C
2024-01-02 19:31:19 - DIMM0 temperature: 0.000C
2024-01-02 19:31:19 - DIMM1 temperature: 0.000C
2024-01-02 19:31:19 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:31:19 - Current CPU temperature: 51C
2024-01-02 19:32:20 - DIMM0 temperature: 0.000C
2024-01-02 19:32:20 - DIMM1 temperature: 0.000C
2024-01-02 19:32:20 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:32:20 - Current CPU temperature: 52C
2024-01-02 19:32:48 - MtSupportRunAllTests - Test execution time: 505.524s (Test 9 cumulative
error count: 0, buffer full count: 0)
2024-01-02 19:32:48 - Running test #10 (Test 10 [Bit fade test, 2 patterns, 1 CPU])
2024-01-02 19:32:48 - MtSupportRunAllTests - Setting random seed to 0x922AD44C
2024-01-02 19:32:48 - MtSupportRunAllTests - Start time: 8710905 ms
2024-01-02 19:32:48 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:32:48 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:32:48 - Start bit fade test (0x0 - 0x41F38000)
2024-01-02 19:32:48 - Starting Stage 0, Pattern=00000000
2024-01-02 19:32:51 - Finished Stage 0, Pattern=00000000
2024-01-02 19:32:51 - Sleep start time: 8713987
2024-01-02 19:32:51 - Sleep time: 300000
2024-01-02 19:32:51 - Test 10 - Sleeping (299 seconds remaining)
2024-01-02 19:33:21 - DIMM0 temperature: 0.000C
2024-01-02 19:33:21 - DIMM1 temperature: 0.000C
2024-01-02 19:33:21 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:33:21 - Current CPU temperature: 36C
2024-01-02 19:33:21 - Slept for 30 seconds
2024-01-02 19:33:51 - Slept for 60 seconds
2024-01-02 19:34:21 - DIMM0 temperature: 0.000C
2024-01-02 19:34:21 - DIMM1 temperature: 0.000C
2024-01-02 19:34:21 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:34:21 - Current CPU temperature: 34C
2024-01-02 19:34:22 - Slept for 90 seconds
2024-01-02 19:34:52 - Slept for 120 seconds
2024-01-02 19:35:22 - DIMM0 temperature: 0.000C
2024-01-02 19:35:22 - DIMM1 temperature: 0.000C
2024-01-02 19:35:22 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:35:22 - Current CPU temperature: 32C
2024-01-02 19:35:22 - Slept for 150 seconds
2024-01-02 19:35:52 - Slept for 180 seconds
2024-01-02 19:36:22 - Slept for 210 seconds
```

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2024-01-02 19:36:22 - DIMM0 temperature: 0.000C
2024-01-02 19:36:23 - DIMM1 temperature: 0.000C
2024-01-02 19:36:23 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:36:23 - Current CPU temperature: 32C
2024-01-02 19:36:53 - Slept for 240 seconds
2024-01-02 19:37:23 - Slept for 270 seconds
2024-01-02 19:37:23 - DIMM0 temperature: 0.000C
2024-01-02 19:37:23 - DIMM1 temperature: 0.000C
2024-01-02 19:37:23 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:37:23 - Current CPU temperature: 32C
2024-01-02 19:37:53 - Starting Stage 1, Pattern=00000000
2024-01-02 19:37:56 - Finished Stage 1, Pattern=00000000
2024-01-02 19:37:56 - Starting Stage 0, Pattern=FFFFFFFF
2024-01-02 19:37:59 - Finished Stage 0, Pattern=FFFFFFFF
2024-01-02 19:37:59 - Sleep start time: 9020243
2024-01-02 19:37:59 - Sleep time: 300000
2024-01-02 19:37:59 - Test 10 - Sleeping (299 seconds remaining)
2024-01-02 19:38:24 - DIMM0 temperature: 0.000C
2024-01-02 19:38:24 - DIMM1 temperature: 0.000C
2024-01-02 19:38:24 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:38:24 - Current CPU temperature: 32C
2024-01-02 19:38:29 - Slept for 30 seconds
2024-01-02 19:39:00 - Slept for 60 seconds
2024-01-02 19:39:24 - DIMM0 temperature: 0.000C
2024-01-02 19:39:24 - DIMM1 temperature: 0.000C
2024-01-02 19:39:24 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:39:25 - Current CPU temperature: 32C
2024-01-02 19:39:30 - Slept for 90 seconds
2024-01-02 19:40:00 - Slept for 120 seconds
2024-01-02 19:40:25 - DIMM0 temperature: 0.000C
2024-01-02 19:40:25 - DIMM1 temperature: 0.000C
2024-01-02 19:40:25 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:40:25 - Current CPU temperature: 31C
2024-01-02 19:40:30 - Slept for 150 seconds
2024-01-02 19:41:00 - Slept for 180 seconds
2024-01-02 19:41:26 - DIMM0 temperature: 0.000C
2024-01-02 19:41:26 - DIMM1 temperature: 0.000C
2024-01-02 19:41:26 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:41:26 - Current CPU temperature: 31C
2024-01-02 19:41:31 - Slept for 210 seconds
2024-01-02 19:42:01 - Slept for 240 seconds
2024-01-02 19:42:26 - DIMM0 temperature: 0.000C
2024-01-02 19:42:26 - DIMM1 temperature: 0.000C
2024-01-02 19:42:26 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:42:26 - Current CPU temperature: 31C
2024-01-02 19:42:31 - Slept for 270 seconds
2024-01-02 19:43:01 - Starting Stage 1, Pattern=FFFFFFFF
2024-01-02 19:43:04 - Finished Stage 1, Pattern=FFFFFFFF
2024-01-02 19:43:04 - MtSupportRunAllTests - Test execution time: 612.591s (Test 10 cumulative
error count: 0, buffer full count: 0)
2024-01-02 19:43:04 - Running test #13 (Test 13 [Hammer test])
2024-01-02 19:43:04 - MtSupportRunAllTests - Setting random seed to 0x80E898D8
2024-01-02 19:43:04 - MtSupportRunAllTests - Start time: 9323790 ms
2024-01-02 19:43:04 - MtSupportRunAllTests - Enabling memory cache for test
2024-01-02 19:43:04 - MtSupportRunAllTests - Enabling memory cache complete
2024-01-02 19:43:05 - Start double-sided hammer test (0x0 - 0x41F380000, Step size: 0x1000000)
2024-01-02 19:43:05 - Using random pattern
2024-01-02 19:43:05 - # of segments to hammer: 7 (SegSize=2048MB, SkipSize=0MB)
2024-01-02 19:43:27 - DIMM0 temperature: 0.000C
2024-01-02 19:43:27 - DIMM1 temperature: 0.000C
2024-01-02 19:43:27 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:43:27 - Current CPU temperature: 42C
2024-01-02 19:43:35 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0xB020000 -
0x8B020000]
2024-01-02 19:44:00 - RunHammerTest - Running hammer test on 2 CPUs (589MB each) [0x8B020000 -
0xD4A39000]
2024-01-02 19:44:28 - DIMM0 temperature: 0.000C
2024-01-02 19:44:28 - DIMM1 temperature: 0.000C
2024-01-02 19:44:28 - iTrueTmp != 0 && usingBlanketOffset == true
```



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2024-01-02 19:44:28 - Current CPU temperature: 43C
2024-01-02 19:44:44 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x100000000 - 0x180000000]
2024-01-02 19:45:09 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x180000000 - 0x200000000]
2024-01-02 19:45:28 - DIMM0 temperature: 0.000C
2024-01-02 19:45:28 - DIMM1 temperature: 0.000C
2024-01-02 19:45:28 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:45:28 - Current CPU temperature: 43C
2024-01-02 19:45:34 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x200000000 - 0x280000000]
2024-01-02 19:46:00 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x280000000 - 0x300000000]
2024-01-02 19:46:25 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x300000000 - 0x380000000]
2024-01-02 19:46:29 - DIMM0 temperature: 0.000C
2024-01-02 19:46:29 - DIMM1 temperature: 0.000C
2024-01-02 19:46:29 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:46:29 - Current CPU temperature: 37C
2024-01-02 19:46:50 - RunHammerTest - Running hammer test on 4 CPUs (512MB each) [0x380000000 - 0x400000000]
2024-01-02 19:47:29 - MtSupportRunAllTests - Test execution time: 262.370s (Test 13 cumulative error count: 0, buffer full count: 0)
2024-01-02 19:47:29 - Finished pass #4 (of 4) (Cumulative error count: 0, buffer full count: 0)
2024-01-02 19:47:29 - Cleanup - Unlocking all memory ranges...
2024-01-02 19:47:29 - All memory ranges successfully unlocked
2024-01-02 19:47:31 - Test result: PASS (Errors: 0)
2024-01-02 19:53:15 - Display test result summary
2024-01-02 19:54:24 - Enabling graphics mode
2024-01-02 19:54:24 - Get screen size
2024-01-02 19:54:24 - Current screen size: 1600 x 1200
2024-01-02 19:54:24 - iTrueTmp != 0 && usingBlanketOffset == true
2024-01-02 19:54:24 - poll_timings_ryzen - [MC0] DramConfiguration=00000120
2024-01-02 19:54:24 - poll_timings_ryzen - [MC0] DebugMisc=000000F8
2024-01-02 19:54:24 - poll_timings_ryzen - [MC1] DramConfiguration=00000120
2024-01-02 19:54:24 - poll_timings_ryzen - [MC1] DebugMisc=000000F8
```